

LCFC Confidential

V1415-IGM GV452/GV552 M/B Schematics Document

Intel Geminilake R-Processor with DDR4 + UMA

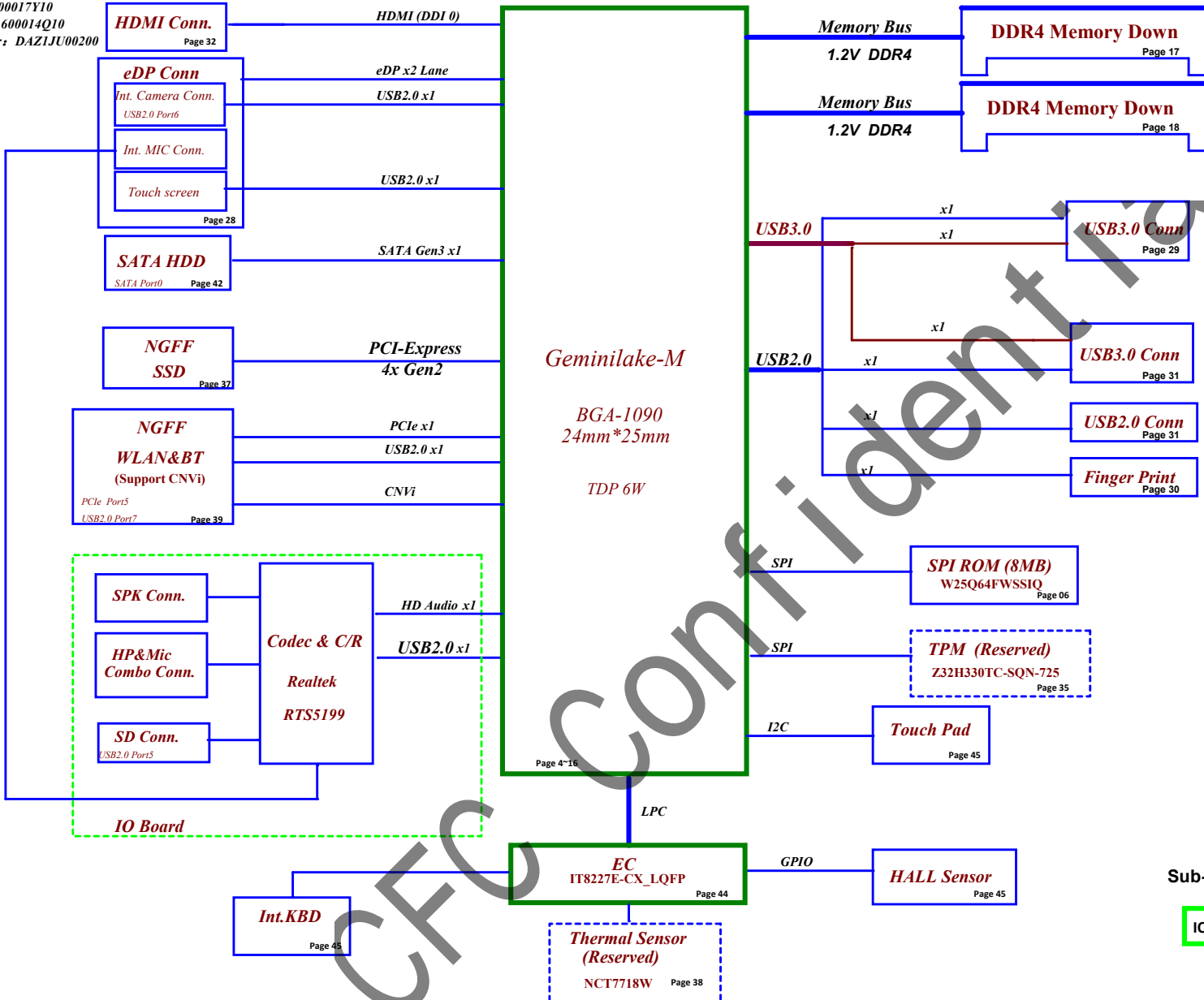
2019-12-24

REV: 1.0

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Board Number: NM-D201
MB Part Number: DA600017Y10
IO/B Part Number: DA600014Q10
14' 15' DAZ Part Number: DAZIJU00200



Sub-board(for 14" 15")
IO BOARD

Voltage Rails (0 --> Means ON , X --> Means OFF)

Power Plane					
State	V20B+ +3VL +5VL	+3VALW +5VALW	+3VALW_SOC +1.24VALW +1.8VALW	+1.2V	+5VS +3VS +1.8VS +1.05VS +0.6VS +CPU_CORE +VNN
S0	0	0	0	0	0
S3	0	0	0	0	X
S5 S4/AC Only	0	0	0	X	X
S5 S4 Battery only	0	X	X	X	X
S5 S4 AC & Battery don't exist	X	X	X	X	X

STATE	SIGNAL	SLP_S0#	SLP_S3#	SLP_S4#	SLP_S5#	+VALW	+V	+VS/VT	Clock
Full ON		HIGH	HIGH	HIGH	HIGH	ON	ON	ON	ON
S0IX(Power On Suspend)		LOW	HIGH	HIGH	HIGH	ON	ON	ON	OFF
S3 (Suspend to RAM)		LOW	LOW	HIGH	HIGH	ON	ON	OFF	OFF
S4 (Suspend to Disk)		LOW	LOW	LOW	HIGH	ON	OFF	OFF	OFF
S5 (Soft OFF)		LOW	LOW	LOW	LOW	ON	OFF	OFF	OFF

USB Port Table

XHCI	Port	Port device
USB 3.0	0	USB3.0
	1	USB3.0
USB 2.0	0	Fingerprint
	1	USB3.0 (2.0)
	2	BT
	3	USB3.0 (2.0)
	4	USB2.0
	5	CARD READER
	6	CAMERA
	7	Touch Screen(RSVD)

DDI PORT LIST

Port	Device
DDI0	HDMI
DDI1	NC
eDP	eDP

PCIe PORT LIST

Port	Device	BIOS Device ID Map	CLK REQ
0	dGPU	PCIe1(Func0):Root Port#3	CLKREQ0
1			
2			
3	LAN	PCIe0(Func0):Root Port#1	CLKREQ1
4			
5	WLAN	PCIe0(Func1):Root Port#2	CLKREQ2

BOM Structure Table

BOM Structure	BTO Item
EMC@	For EMC part
EMC_NS@	For EMC un-stuff part
EMC_15@	EMC 15" part
14@	For 14" part
15@	For 15" part
RF@	For RF part
CD@	Cost Down part
DIS@	DIS SKU ID part
UMA@	UMA SKU ID part
IGM@	IGM CPU SKU part
IGMR@	IGMR CPU SKU part
LBG@	LBG project SKU part
NEC@	NEC project SKU part
CNVI@	CNVI SKU part
TMSEN@	Thermal Sensor part
TMSEN_UMA@	UMA Thermal Sensor part
TPM@	TPM part
Debug@	USB debug feature part
USB@	Non USB debug feature part
TS@	Touch Screen part
TS_LBG@	LBG project Touch Screen part
TS_NEC@	NEC project Touch Screen part
UART@	UART debug part
RTCRST@	Clear RTCRST# function part
ME@	ME part
@	un-stuff part
HDMI@	HDMI Logo part
N4100@	GLK N4100 CPU part
N4000@	GLK N4000 CPU part
N5000@	GLK N5000 CPU part
N4100_QS@	GLK N4100 QS CPU part
HDA18@	HDA Bus 1.8V power part
HDA33@	HDA Bus 3.3V power part
NM_C111@	MB PCB part
NS_C121@	ODD PCB part

SMBUS Control Table

	SOURCE	VGA	BATT	IT8986HE	SODIMM	WLAN WIMAX	Thermal Sensor	PCH	TP Module	Charger	PMIC
EC_SMB_CK0 EC_SMB_DA0	EC +3VL	X	X	V	X	X	X	X	X	X	V
EC_SMB_CK1 EC_SMB_DA1	EC +3VL	X	V	V +3VL	X	X	X	X	X	V	X
EC_SMB_CK2 EC_SMB_DA2	EC +3VS	X	X	V +3VS	X	X	V	X	X	X	X
PCH_SMB_CLK PCH_SMB_DATA	PCH +3VALW_SOC	X	X	X	V +3VS	V +3VS	X	V +3VALW_PCH	X	X	X

EC SM Bus0 address		EC SM Bus1 address		EC SM Bus2 address		PCH SM Bus address	
Device	Address	Device	Address	Device	Address	Device	Address
PMIC	0x68	Smart Battery Charger	0x16 0x12	Thermal Sensor	0x98(reserve)	DDR SODIMM Wlan	0xA0 Rsvd

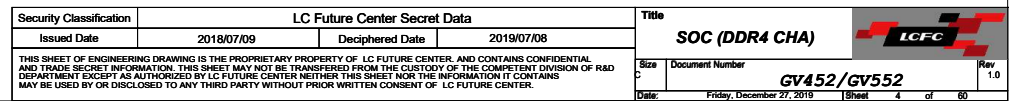
I2C4/I2C7 Bus address (Touch Pad)

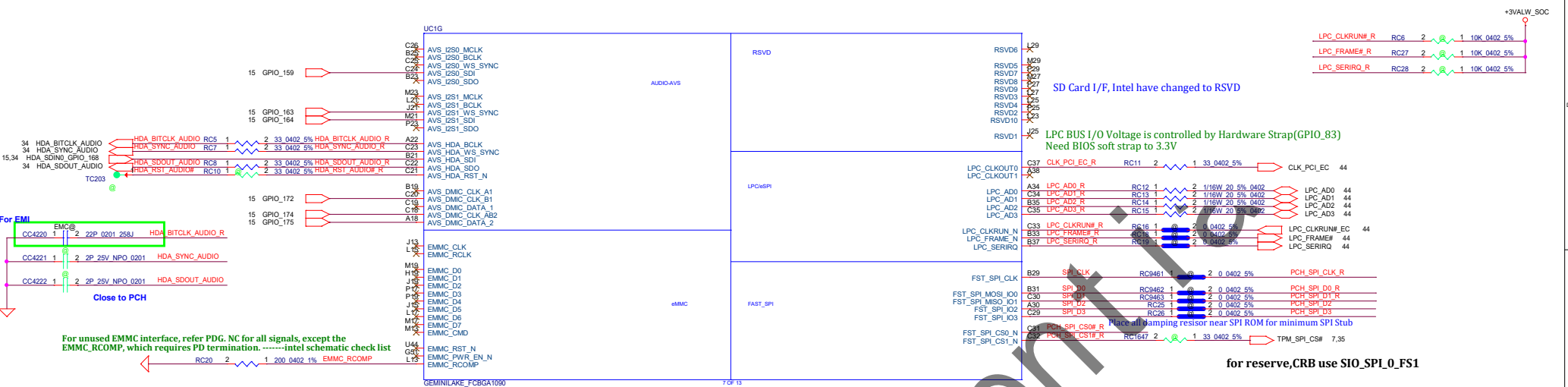
Device	Address
Slave	0x15
Descriptor	0x0001

RCOMP RESISTOR REQUIREMENT

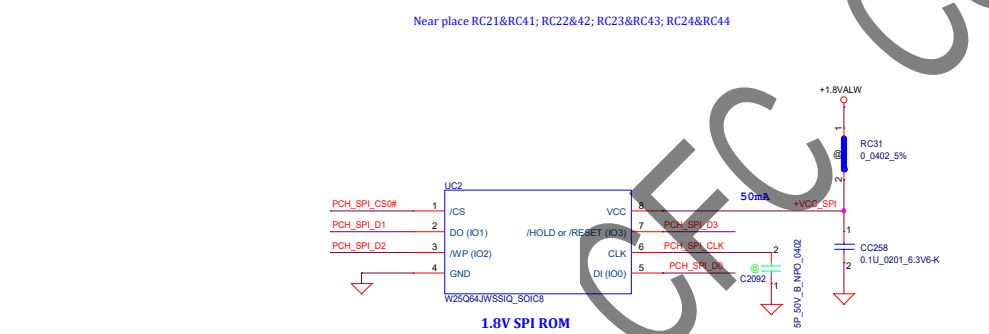
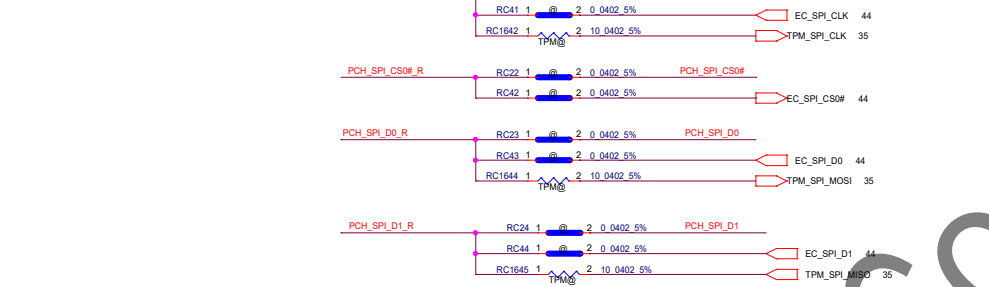
INTERFACE	PIN NAME	LOCATION	VALUE(ohm)
Memory	MEM_CH0_RCOMP	RC1	110 +/-1%
	MEM_CH1_RCOMP	RC2	110 +/-1%
USB2	USB2_RCOMP	RC64	113 +/-1%
USB3/PCIe/SATA	PCIe2_USB3_SATA3_RCOMP_P/N	RC63	100 +/-1%
PCIe Refclk	PCIe_REF_CLK_RCOMP	RC62	50 +/-1%
DP/eDP*/HDMI*	EDP_RCOMP_P/N	RC79	100 +/-1%
MDSI	MDSI_RCOMP	RC78	150 +/-1%
CNVi	CNV_WT_RCOMP	RC48	150 +/-1%
SMBUS/GPIO/EMMC for all 1.8V only and 1.8V mode operation of 1.8/3.3V CFIO interfaces	EMMC_RCOMP	RC20	200 +/-1%

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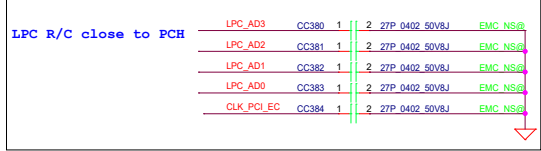
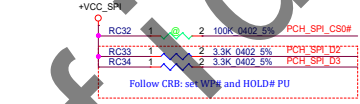


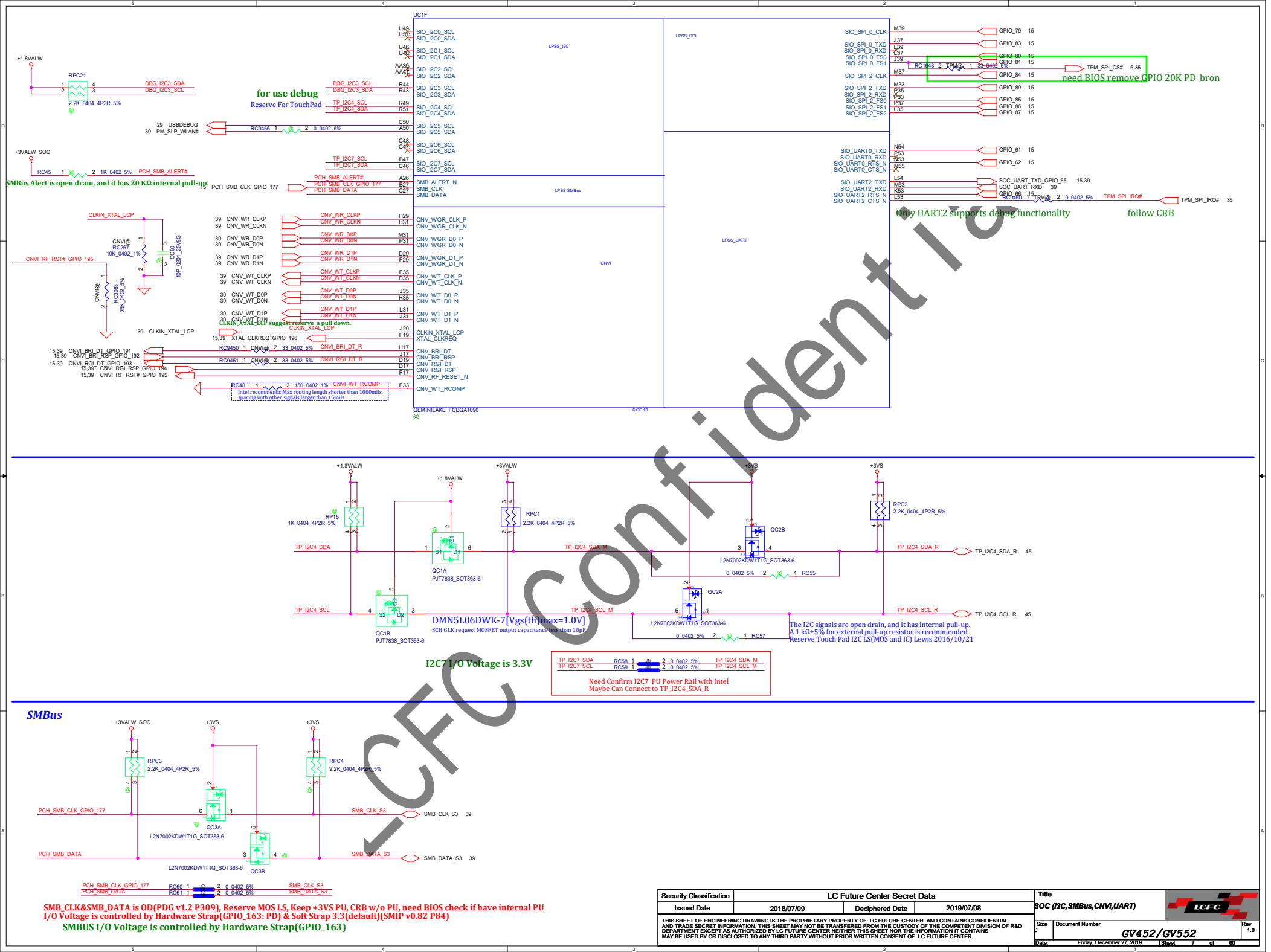


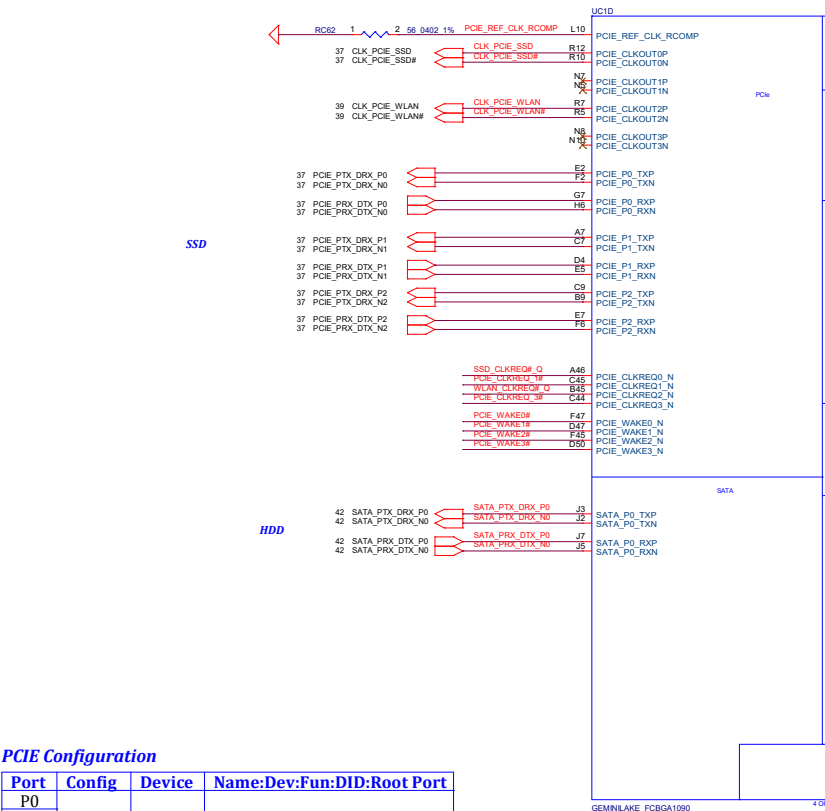
SPI ROM



Ball Name	Signal Name	I/O Voltage	Default Term	Buffer Type
FST_SPI_CS0_N	PCH_SPI_CS0#	1.8V	Native	HSMV
FST_SPI_MISO_I00	PCH_SPI_D0	1.8V	Native	HSMV
FST_SPI_MISO_I01	PCH_SPI_D1	1.8V	Native	HSMV
FST_SPI_IO2	PCH_SPI_D2	1.8V	Native	HSMV
FST_SPI_IO3	PCH_SPI_D3	1.8V	Native	HSMV
FST_SPI_CLK	PCH_SPI_CLK	1.8V	Native	HSMV







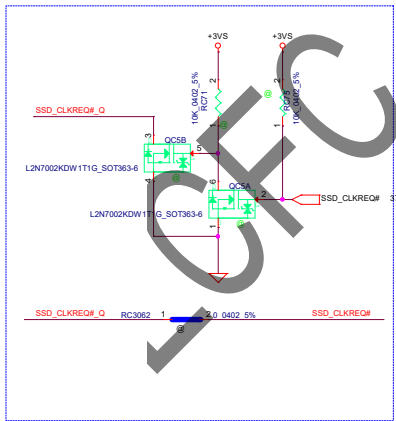
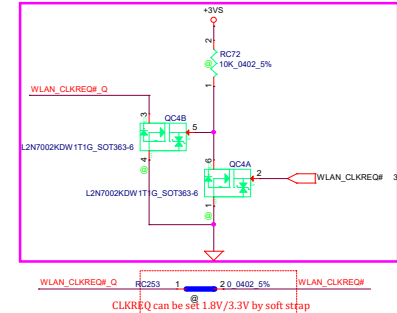
PCIE Configuration

Port	Config	Device	Name:Dev:Fun:DID:Root Port
P0			
P1	X4	SSD	PCIe1(Func0):19:0:0x31D8:2
P2			
P3			
P4	X1		PCIe0(Func0):20:0:0x31D6:0
P5	X1	WLAN	PCIe0(Func1):20:1:0x31D7:1

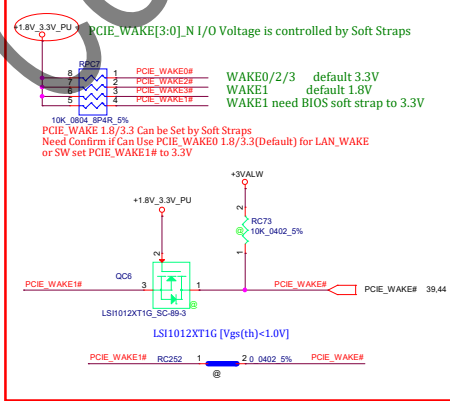
CLOCK REQUEST



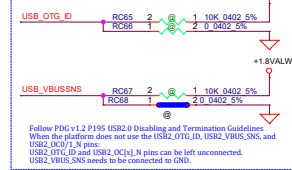
Need Check
CLKREQ0 can be set 1.8V/3.3V by soft strap
CLKREQ0/2/4 default 3.3V
CLKREQ1 default 1.8V
PCI_CLKREQ[3:0] N/I/O Voltage is controlled by Soft Straps



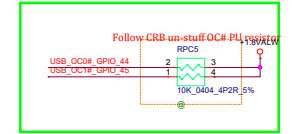
LAN WAKE



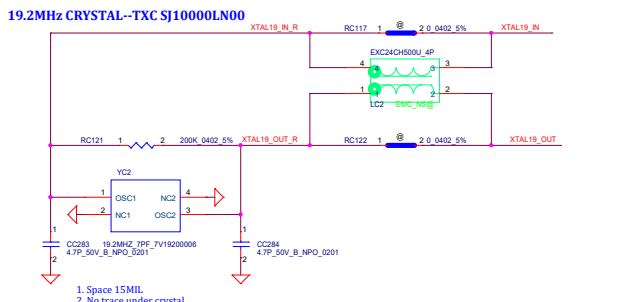
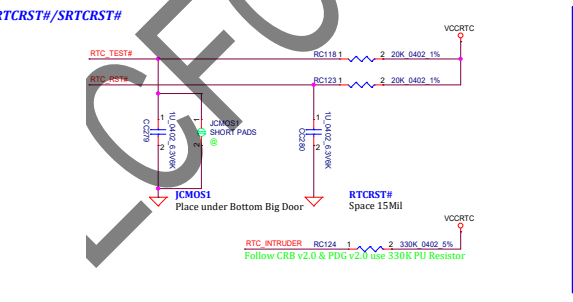
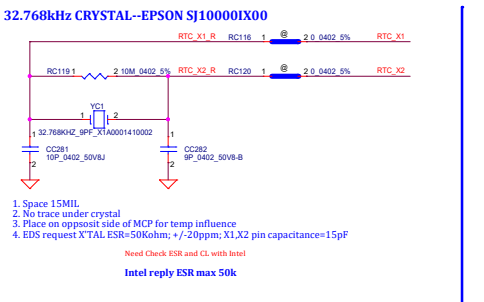
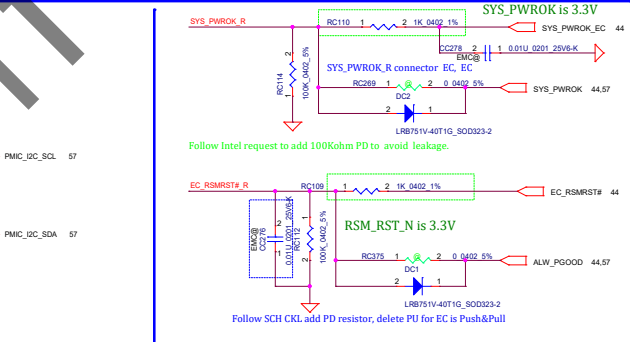
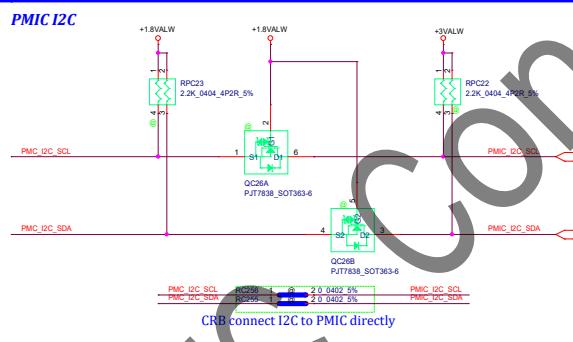
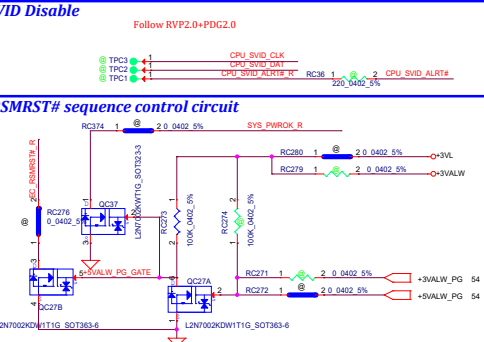
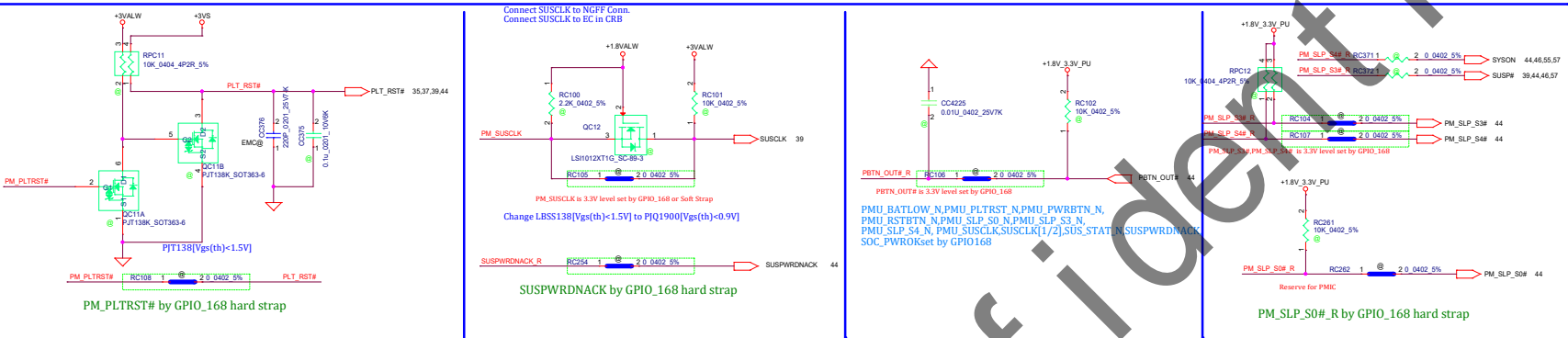
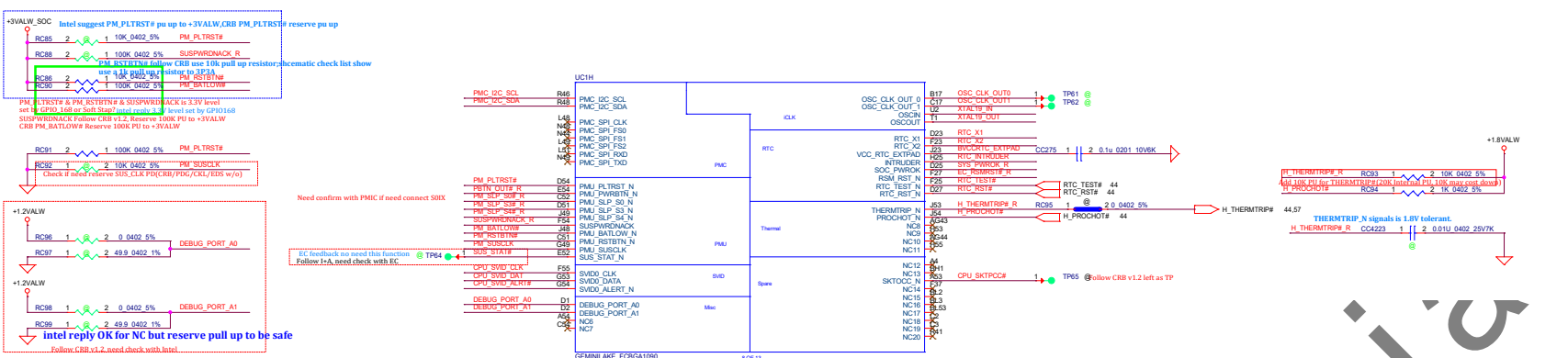
USB DUAL ROLE

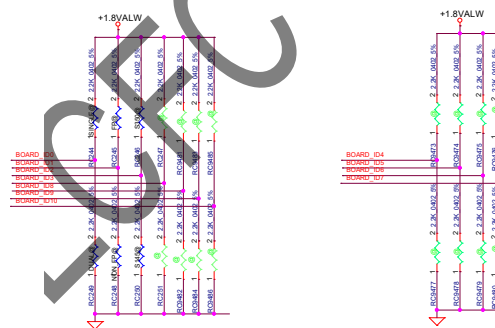
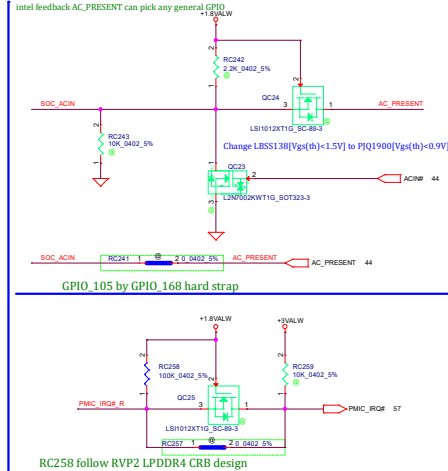
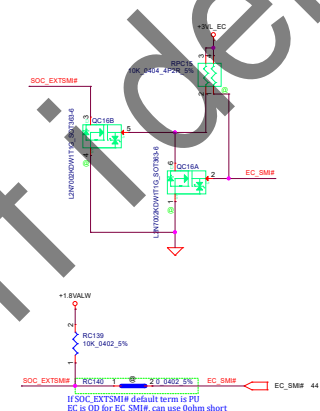
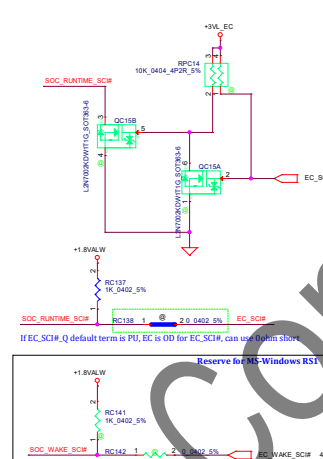
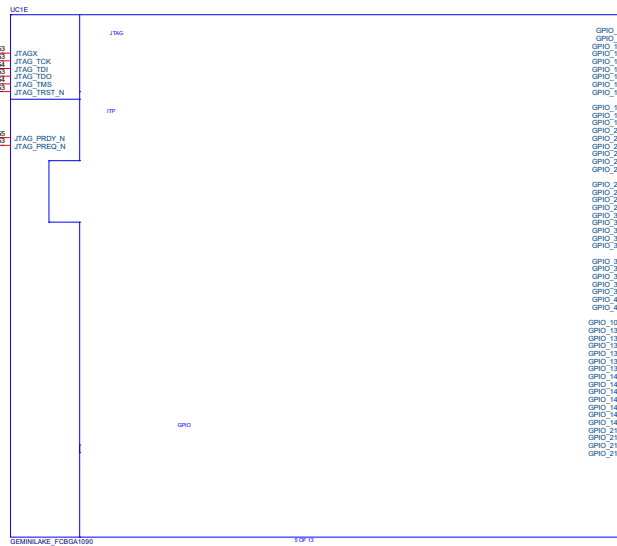


USB OCP



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Device Board ID

ID0	ID1	ID2	ID3	ID8	Description
0					DDR4 DUAL channel
1					DDR4 SINGLE channel
	0				Non Finger Print
	1				Finger Print
		0			\$145 touch pad
		1			\$350 touch pad
			0		
			1		
				0	
				1	

RAM Board ID


ID4	ID5	ID6	ID7	Description
0	0	0	0	SAMSUNG K4A8G16SW-BCWE
0	0	0	1	MICRON MT40A512M16TB-062EJ
0	0	1	0	HYNIX H5AN8G6CJR-XNC
0	0	1	1	Samsung K4A8G16SW-BCTD
0	1	0	0	Hynix H5AN8G6CJR-YKC
0	1	0	1	
0	1	1	0	
0	1	1	1	

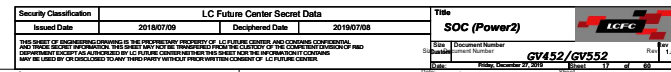
UC1K		
A3	VSS_6	AF44
A6	VSS_13	AF45
A12	VSS_1	AF47
A16	VSS_11	AF48
A20	VSS_2	AF50
A24	VSS_3	AF52
A28	VSS_4	AF53
A32	VSS_5	AF55
A36	VSS_7	AG20
A40	VSS_8	AL21
A44	VSS_9	AG25
A48	VSS_10	AG29
A51	VSS_11	AG35
AA12	VSS_12	AG38
AA13	VSS_14	AG38
AA15	VSS_15	AG38
AA17	VSS_16	AG38
AA21	VSS_17	AG38
AA23	VSS_18	AG38
AA25	VSS_19	AG38
AA27	VSS_20	AG38
AA35	VSS_21	AG38
AA43	VSS_22	AG38
AA48	VSS_23	AG38
AB1	VSS_24	AG38
AB3	VSS_25	AG38
AB5	VSS_26	AG38
AC3	VSS_27	AG38
AC13	VSS_33	AG38
AC23	VSS_28	AG38
AC25	VSS_29	AG38
AC27	VSS_30	AG38
AC29	VSS_31	AG38
AE18	VSS_32	AG38
AE23	VSS_34	AG38
AE25	VSS_35	AG38
AE27	VSS_36	AG38
AE3	VSS_37	AG38
AE48	VSS_38	AG38
AF3	VSS_39	AG38
AF4	VSS_40	AG38
AF6	VSS_41	AG38
AF9	VSS_42	AG38
AF11	VSS_43	AG38
AF12	VSS_44	AG38
AF14	VSS_45	AG38
AF16	VSS_46	AG38
AF18	VSS_47	AG38
AF23	VSS_48	AG38
AF25	VSS_49	AG38
AF29	VSS_50	AG38
AF40	VSS_51	AG38
AF42	VSS_52	AG38

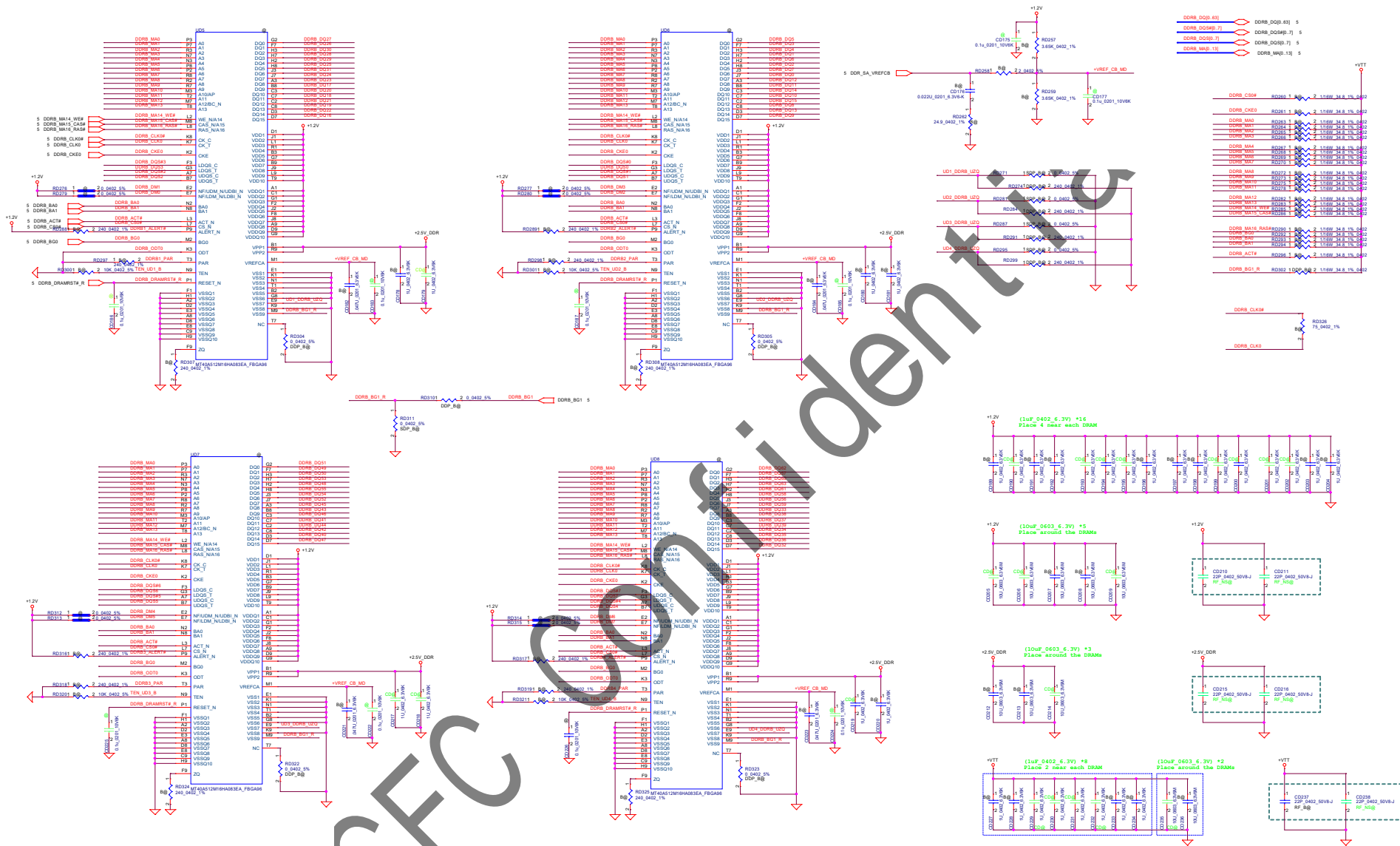
UC1L		
AN48	VSS_111	BC11
AN49	VSS_112	BC17
AN51	VSS_113	BC19
AN53	VSS_114	BC21
AP23	VSS_115	BC23
AP27	VSS_116	BC25
AP28	VSS_117	BC27
AP29	VSS_118	BC33
AP33	VSS_119	BC35
AP35	VSS_120	BC37
AR2	VSS_121	BC39
AR7	VSS_122	BC41
AR10	VSS_123	BC45
AR12	VSS_124	BC51
AR17	VSS_125	BD9
AR30	VSS_126	BD15
AR44	VSS_127	BD19
AR46	VSS_128	BD21
AR40	VSS_129	BD28
AR54	VSS_130	BD35
AT23	VSS_131	BD37
AT33	VSS_132	BD47
AU3	VSS_133	BE3
AU10	VSS_134	BE28
AU28	VSS_135	BE53
AU46	VSS_136	BF9
AU53	VSS_137	BF19
AV15	VSS_138	BF37
AV17	VSS_139	BF47
AV23	VSS_140	BG1
AV25	VSS_141	BG6
AV31	VSS_142	BG28
AV33	VSS_143	BG50
AV39	VSS_144	BG55
AW41	VSS_145	BH11
AW2	VSS_146	BH13
AW5	VSS_147	BH17
AW10	VSS_148	BH19
AW28	VSS_149	BH23
AW46	VSS_150	BH25
AW51	VSS_151	BH28
AW54	VSS_152	BH31
AW13	VSS_153	BH33
AW15	VSS_154	BH37
AW28	VSS_155	BH39
AW41	VSS_156	BH41
AW43	VSS_157	BH45
B2	VSS_158	BJ2
B55	VSS_159	BJ15
BA27	VSS_160	BJ19
BA29	VSS_161	BJ25
BB1	VSS_162	BJ28
BB28	VSS_163	BJ31
BB55	VSS_164	BJ37
BC5	VSS_178	BJ41

UC1M		
AL23	VSS_221	J51
BL54	VSS_222	K1
BK17	VSS_223	K3
BK21	VSS_224	K5
BK35	VSS_225	K7
BK39	VSS_226	K9
BK53	VSS_227	L5
BL5	VSS_228	L7
BL8	VSS_229	L9
BL10	VSS_230	L13
BL14	VSS_231	L15
BL24	VSS_232	L19
BL28	VSS_233	L23
BL32	VSS_234	L25
BL42	VSS_235	L27
BL46	VSS_236	L29
BL51	VSS_237	L33
C1	VSS_238	L35
C16	VSS_239	L37
C12	VSS_240	L39
C28	VSS_241	L43
C36	VSS_242	L45
D5	VSS_243	L47
D9	VSS_244	L49
D21	VSS_245	L51
D28	VSS_246	L53
D41	VSS_247	L55
D45	VSS_248	L57
D55	VSS_249	L59
E28	VSS_250	L61
E55	VSS_251	L63
F1	VSS_252	L65
F4	VSS_253	L67
F21	VSS_254	L69
F31	VSS_255	L71
G8	VSS_256	L73
H13	VSS_257	L75
H16	VSS_258	L77
H21	VSS_259	L79
H23	VSS_260	L81
H28	VSS_261	L83
H33	VSS_262	L85
H39	VSS_263	L87
J8	VSS_264	L89
J27	VSS_265	L91
J33	VSS_266	L93
J41	VSS_267	L95
J45	VSS_268	L97
J45	VSS_269	L99
J45	VSS_270	M1
J45	VSS_271	M3


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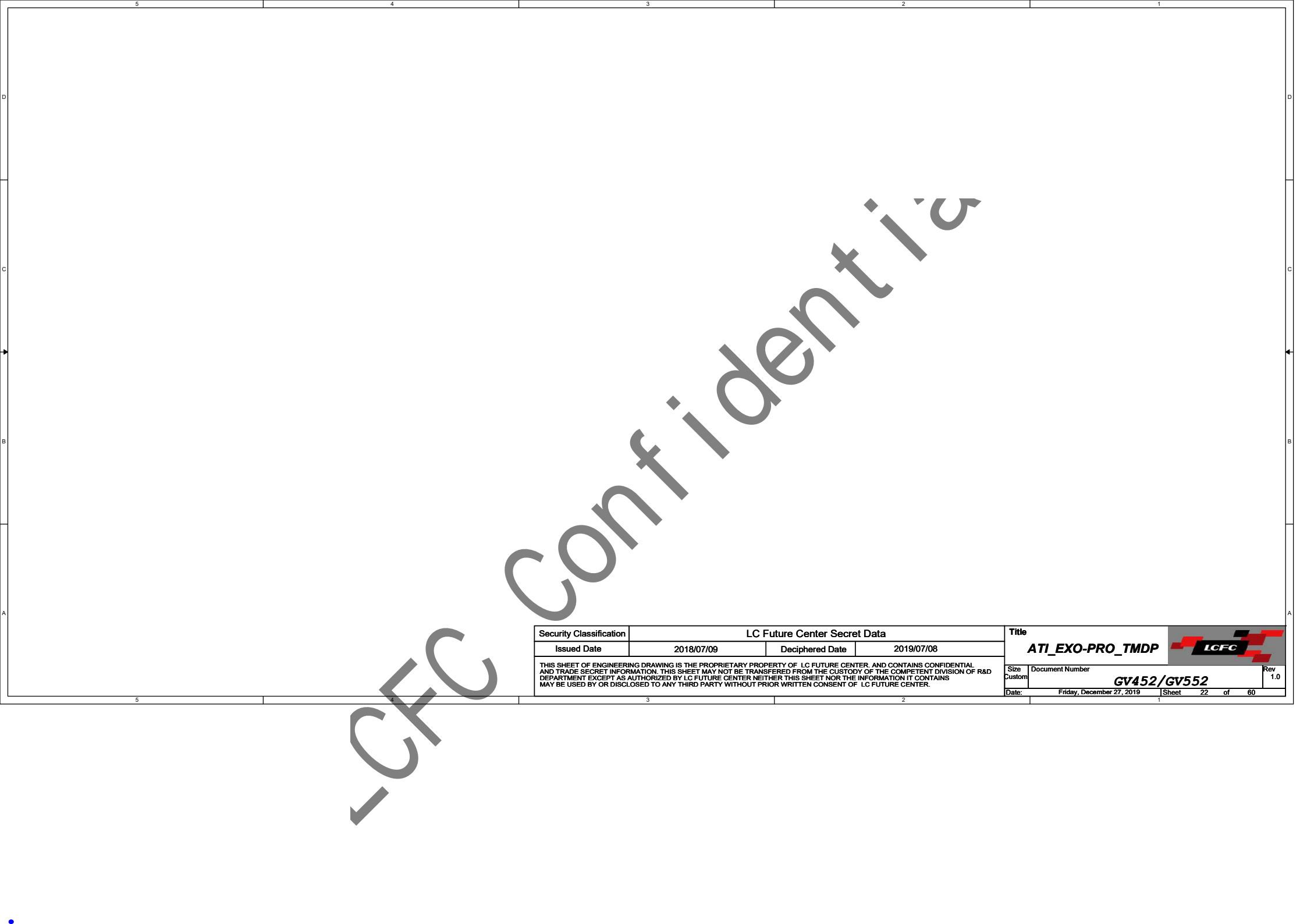
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				<small>Date</small>	<small>Sheet</small>
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Issue	Date		10/15/2019		

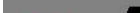
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Need Open

V20B+ **2A 80 mil**

J28 1 2

JUMP_43X39

F2

3A_32V_0497003PKRH-F

+LEDVDD **2A 80 mil**

C5002 1 2

4.7uF_0805_25V6K

C25 1 2

0.1uF_0801_25V6K

C23 1 2

EMC

C23 0.1u for G HSW panel PCB issue


44 CMOS_ON#

9 CPU_EDP_TX0+ CPU_EDP_TX0- CPU_EDP_TX0-

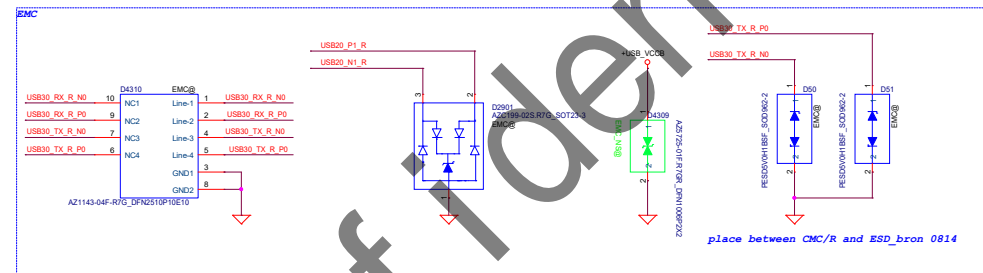
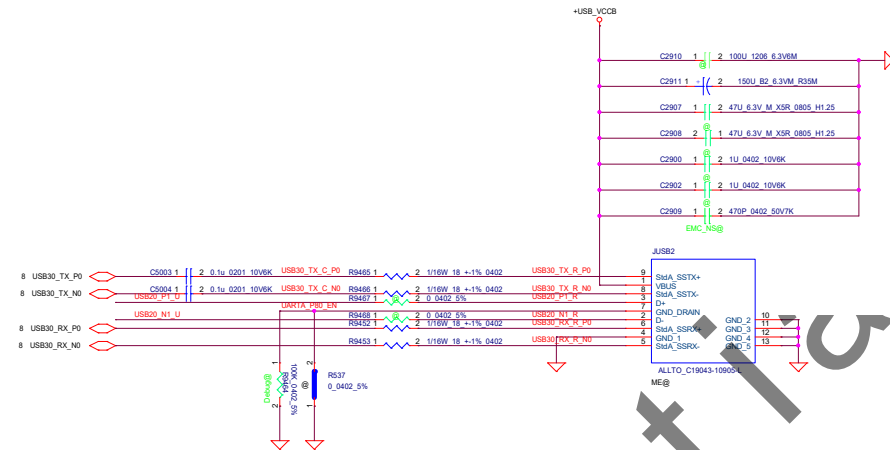
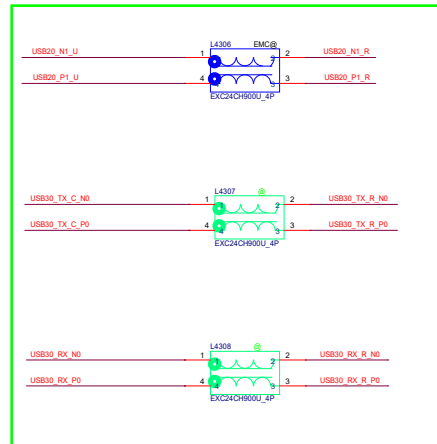
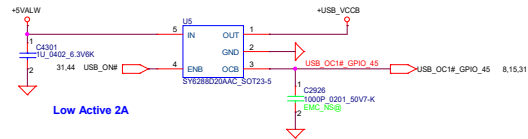
PCB layout for the USB to UART module. The layout includes various components and dimensions:

- Top Section:**
 - Components: J2, JUMP_43X39, LP2301ALT1G_SOT23-3, Q7, C5, C6, C3, C9, C10, C19, C16, C17, C18, C20, C21, C1320, R182, R183.
 - Dimensions: W=40 mils, W=60 mils.
 - Note: "For EMI Close to R5"
- Middle Section:**
 - Components: F10, NEC@, 0.5A_32V_ERBRDOR50X, SP, C4864.
 - Dimensions: W=40mils.
- Bottom Section:**
 - Connectors: +LEDVDD, JEDP1.
 - Dimensions: W=60 mils.
 - Text: "w for eDP panel"
- Bottom Right:**
 - Text: "DRAPH1_FC5AF301-3181H ME@"

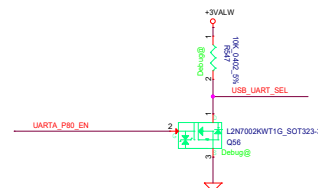
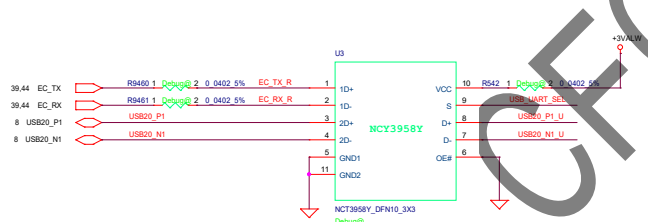
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				JShigel	28 of 80		

RIGHT SIDE USB3.0 PORT



For USB Debug Function

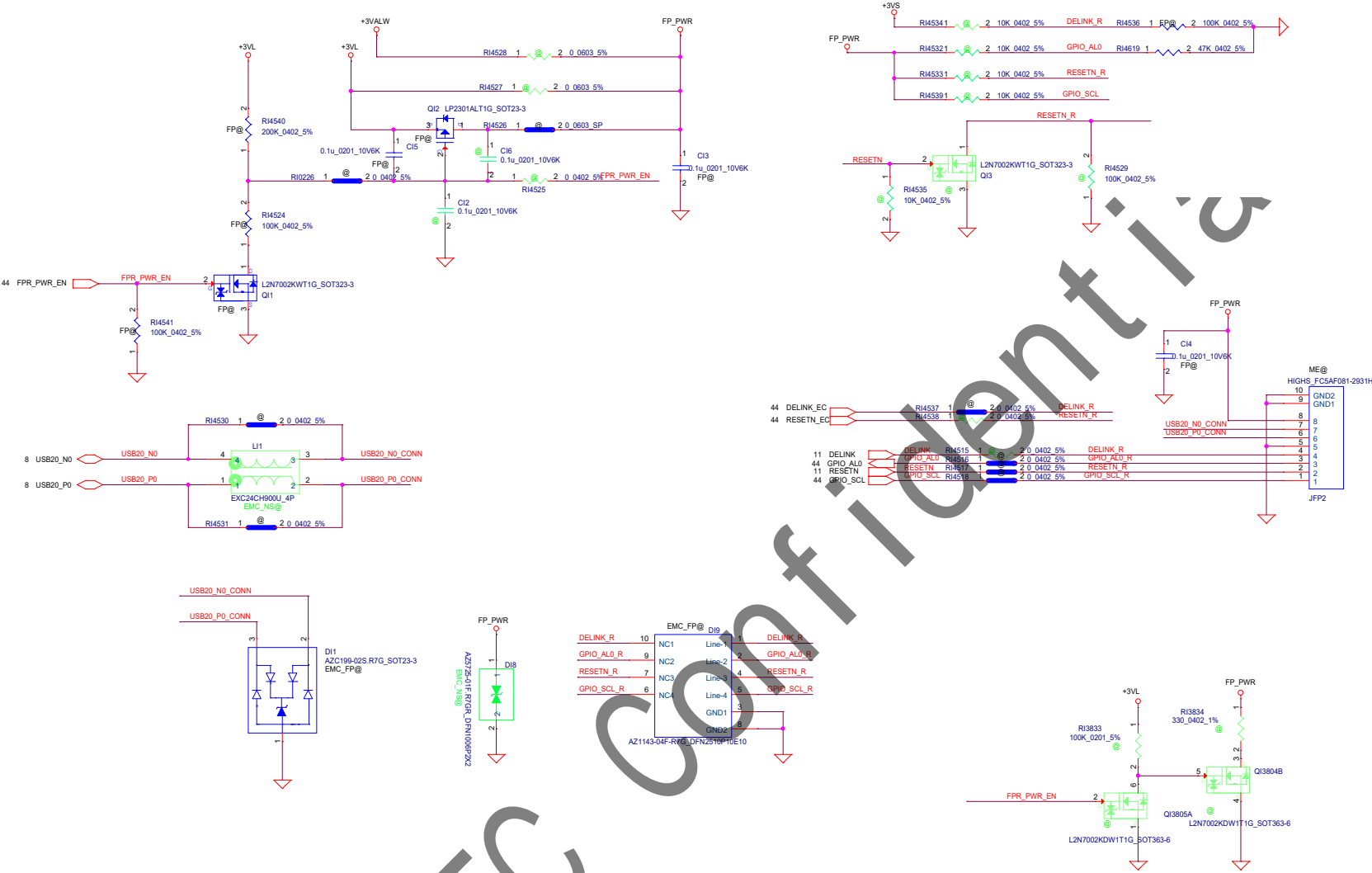


USBDEBUG	Kernel debug
Set input	Set input
Set output Low	ENABLE

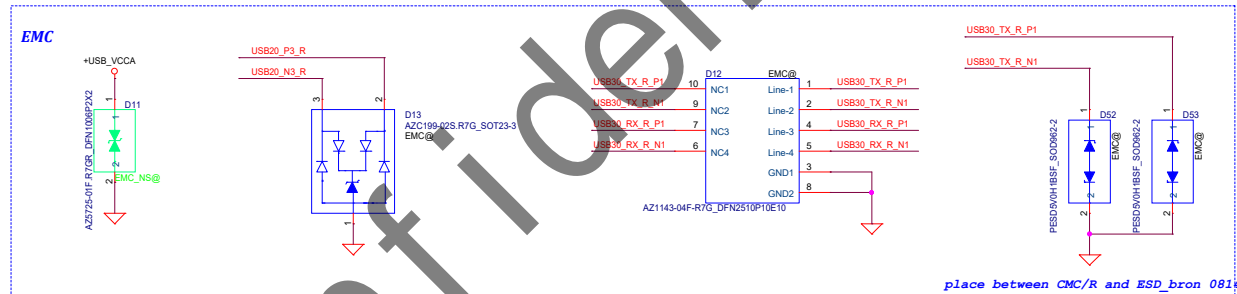
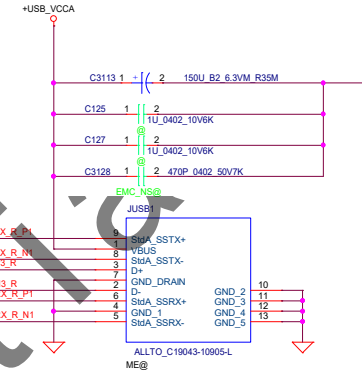
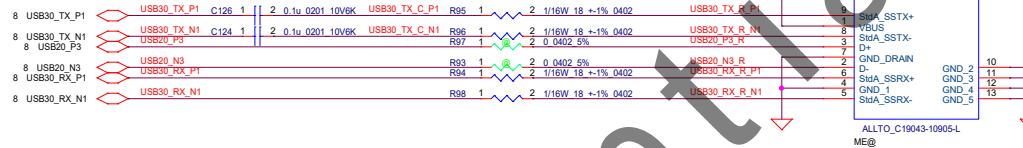
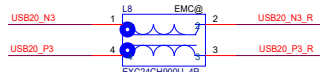
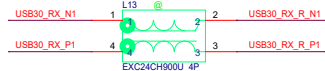
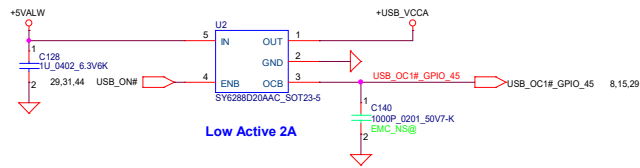
UARTA_P80_EN	POST 80
Set input	DISABLE
Set output Low	ENABLE

OE#	S	FUNCTION
H	X	DISABLE
L	L	D(+/-) to 1D(+/-)
L	H	D(+/-) to 2D(+/-)

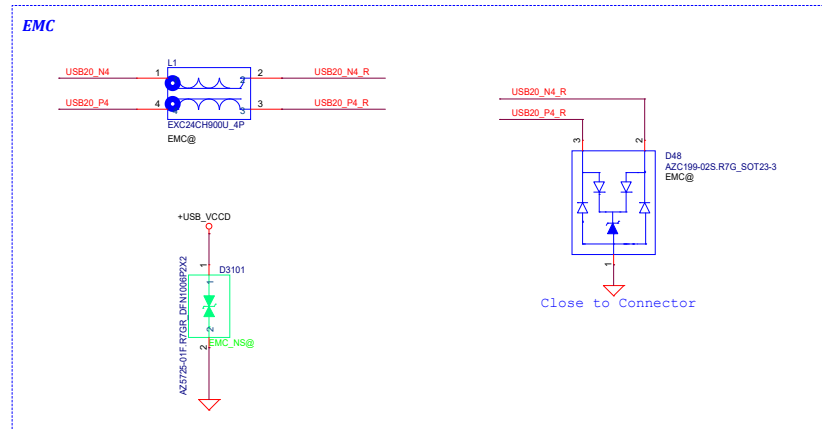
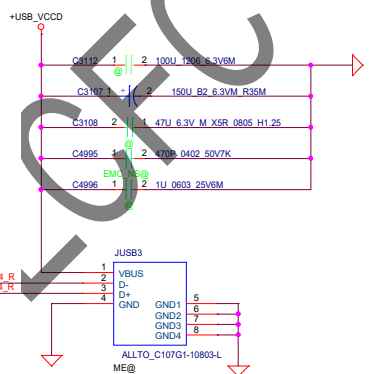
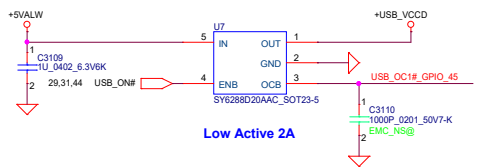
Finger Print



LEFT SIDE USB3.0 PORT x1

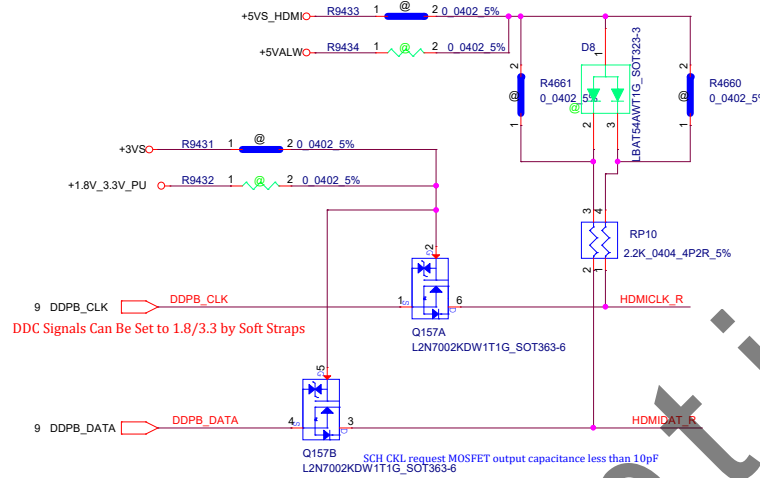
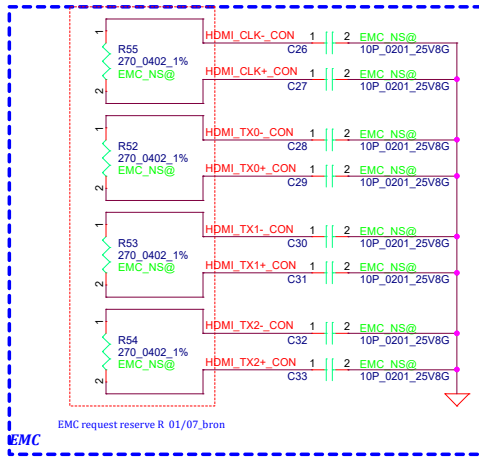


place between CMC/R and ESD_bron 0814

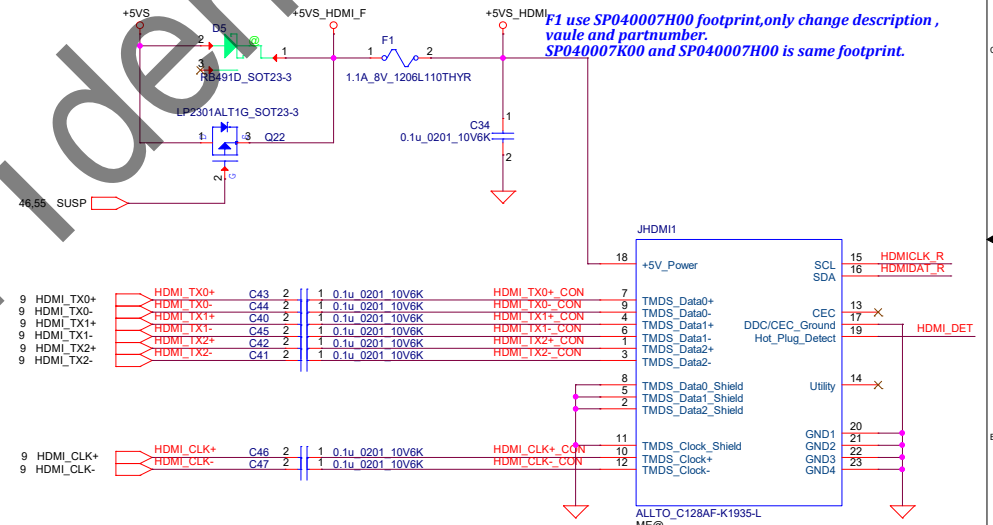
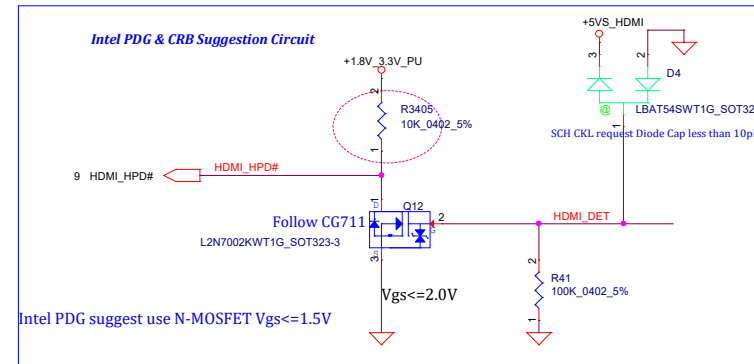
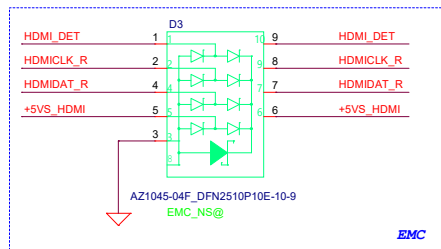
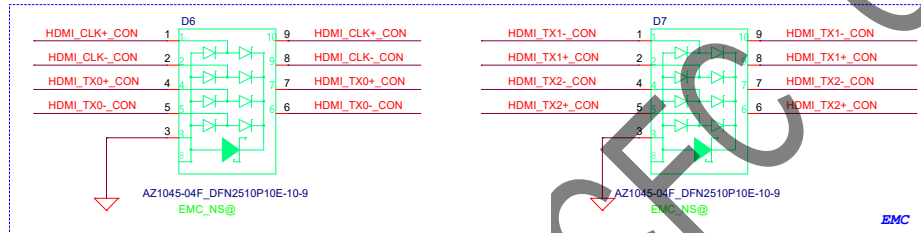
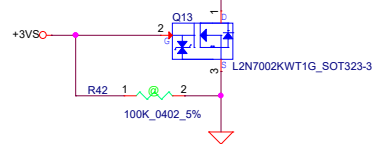
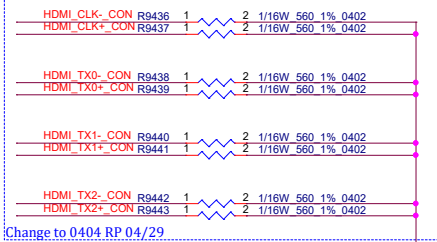


Close to Connector

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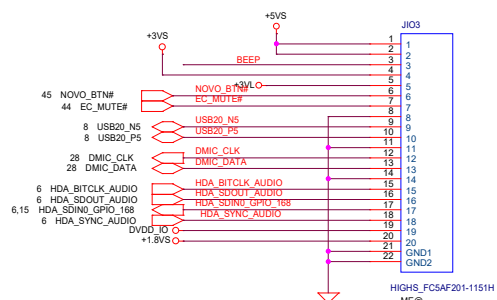
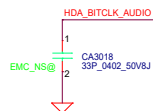
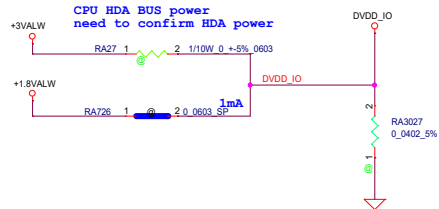
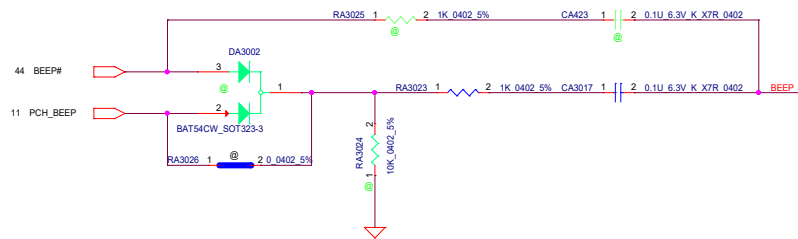
Follow PDG & CRB use 470ohm,SVT change 470 to 560 for HDMI issue



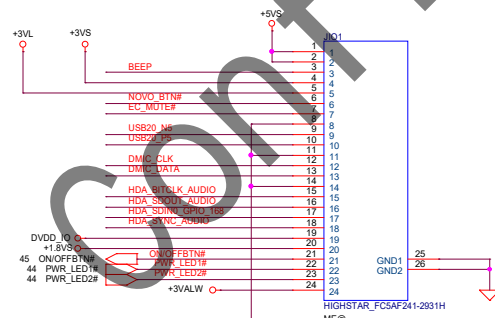
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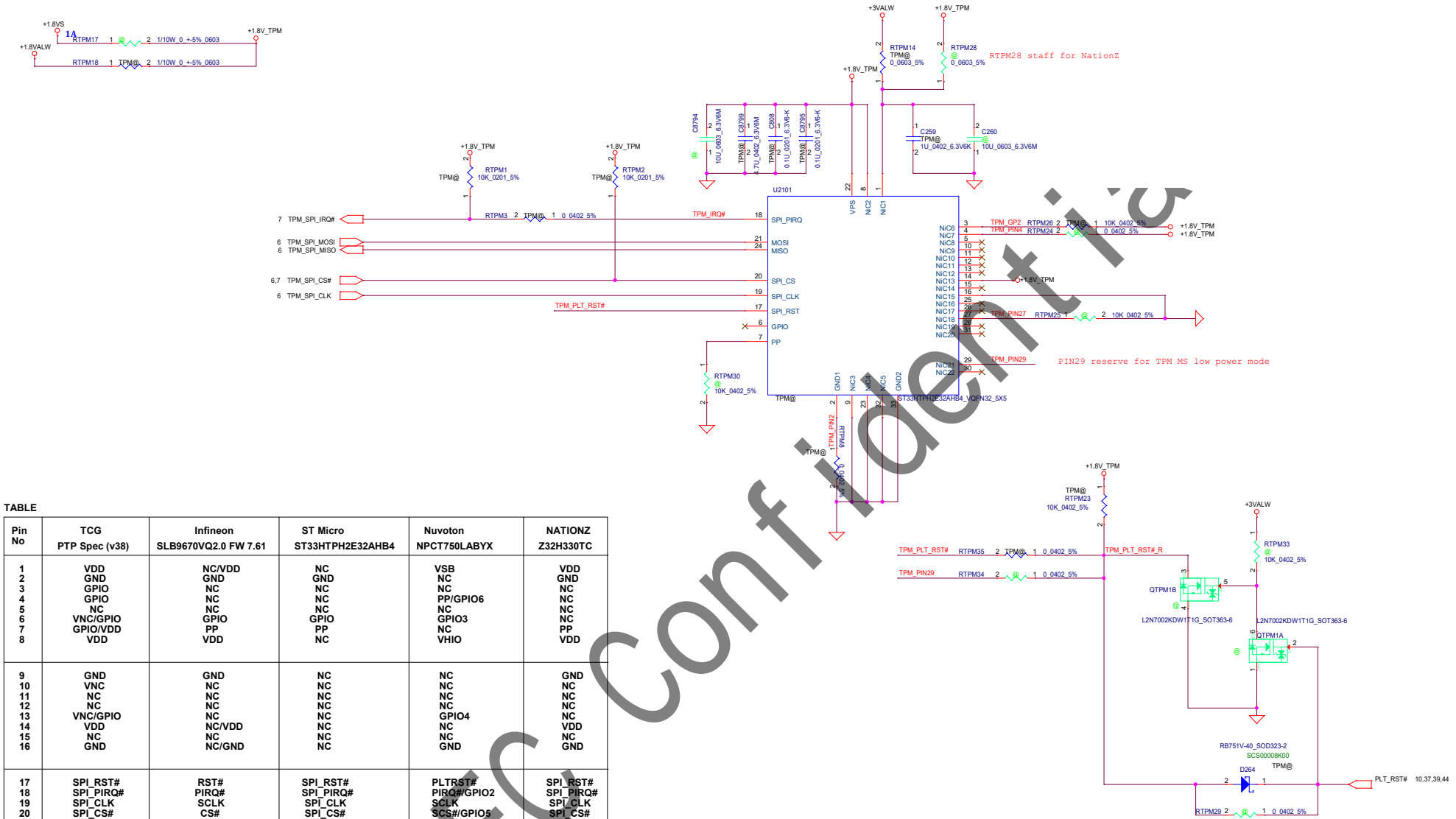
20Pin CONN




24Pin CONN

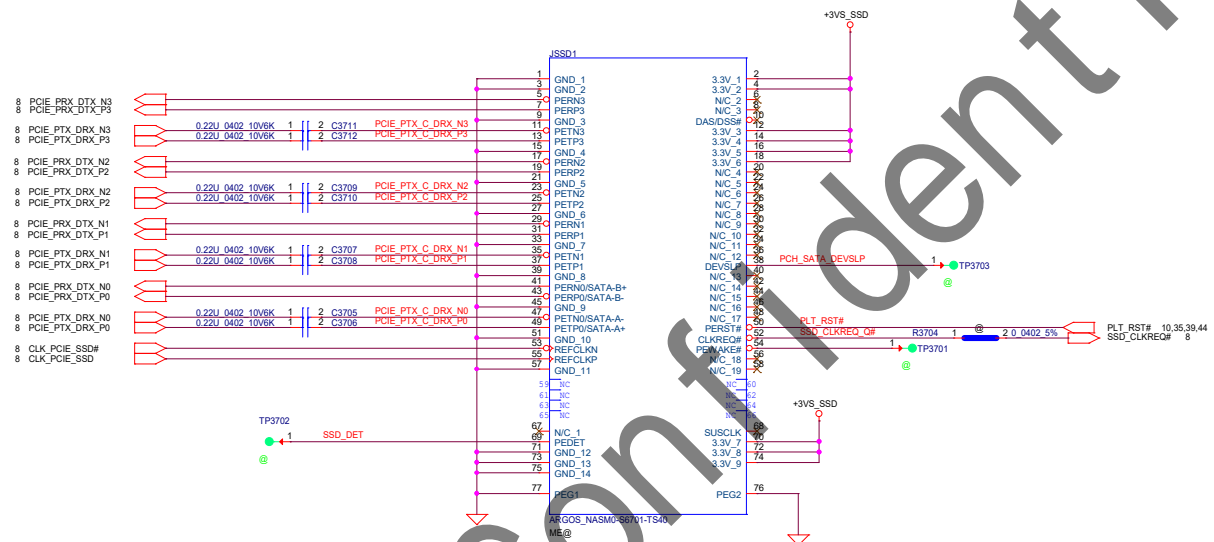
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1	VDD	NC/VDD	NC	VSB	VDD
2	GND	GND	NC	NC	GND
3	GPIO	NC	NC	NC	NC
4	GPIO	NC	NC	PP/GPIO6	NC
5	NC	NC	NC	NC	NC
6	VNC/GPIO	GPIO	GPIO	GPIO3	NC
7	GPIO/VDD	PP	PP	NC	PP
8	VDD	VDD	NC	VHIO	VDD
9	GND	GND	NC	NC	GND
10	VNC	NC	NC	NC	NC
11	NC	NC	NC	NC	NC
12	NC	NC	NC	NC	NC
13	VNC/GPIO	NC	NC	GPIO4	NC
14	VDD	NC/VDD	NC	NC	VDD
15	NC	NC	NC	NC	NC
16	GND	NC/GND	NC	GND	GND
17	SPI_RST#	RST#	SPI_RST#	PLTRST#	SPI_RST#
18	SPI_PIRQ#	PIRQ#	SPI_PIRQ#	PIRQ#	SPI_PIRQ#
19	SPI_CLK	SCLK	SPI_CLK	SCLK	SPI_CLK
20	SPI_CS#	CS#	SPI_CS#	SCS#	SPI_CS#
21	MOSI	MOSI	MOSI	MOSI/GPIO5	MOSI
22	VDD	VDD	VPS	VHIO	VDD
23	GND	GND	NC	GND	GND
24	MISO	MISO	MISO	MISO	MISO
25	NC	NC	NC	NC	NC
26	NC	NC	NC	NC	NC
27	NC	NC	NC	NC	NC
28	NC	NC	NC	NC	NC
29	VNC/GPIO	NC	NC	SDA/GPIO0	NC
30	VNC/GPIO	NC	NC	SCL/GPIO1	NC
31	VNC	NC	NC	NC	NC
32	GND	GND	NC	NC	GND



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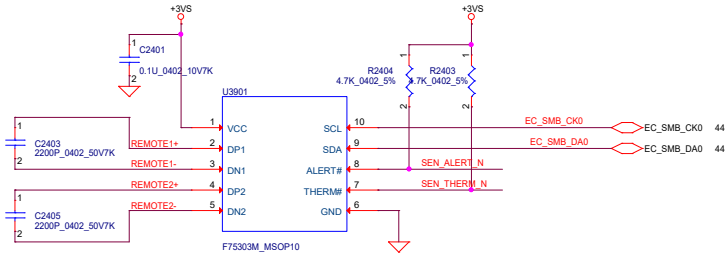
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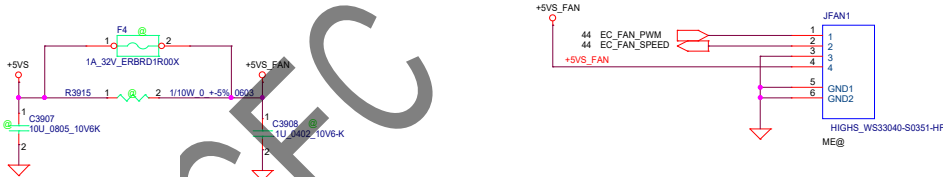
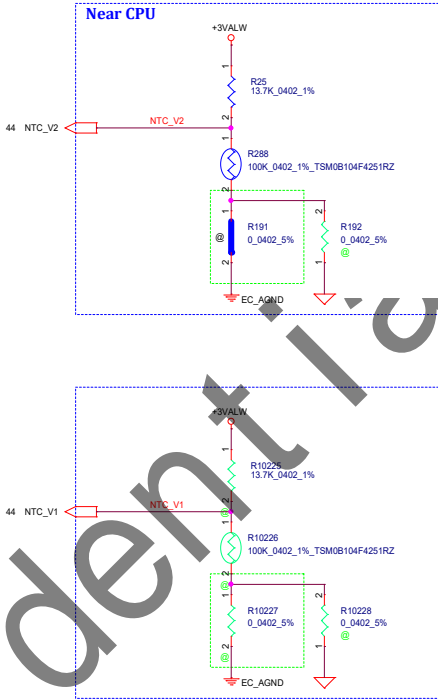
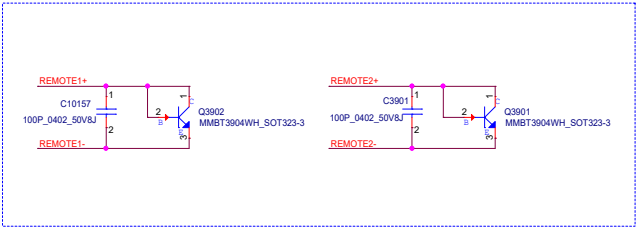
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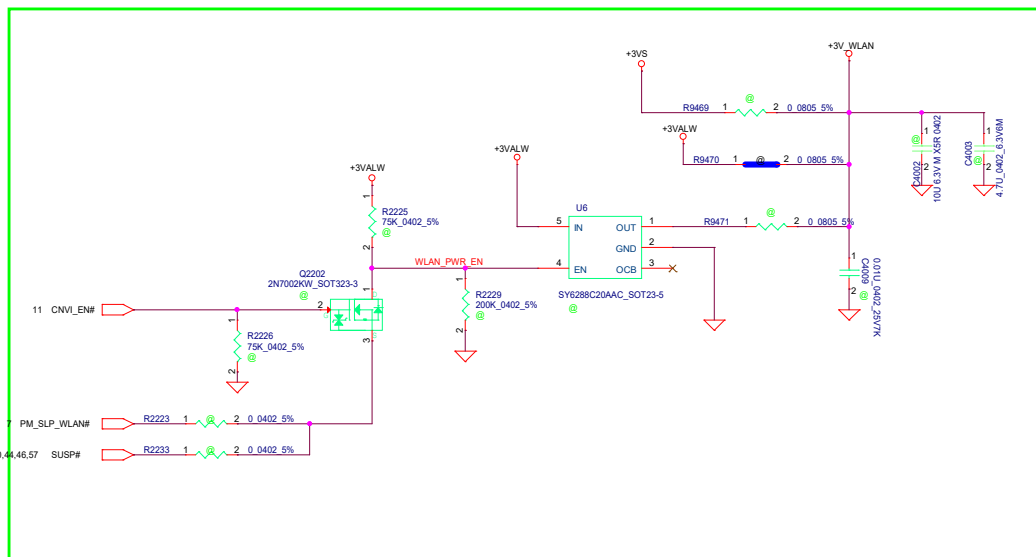
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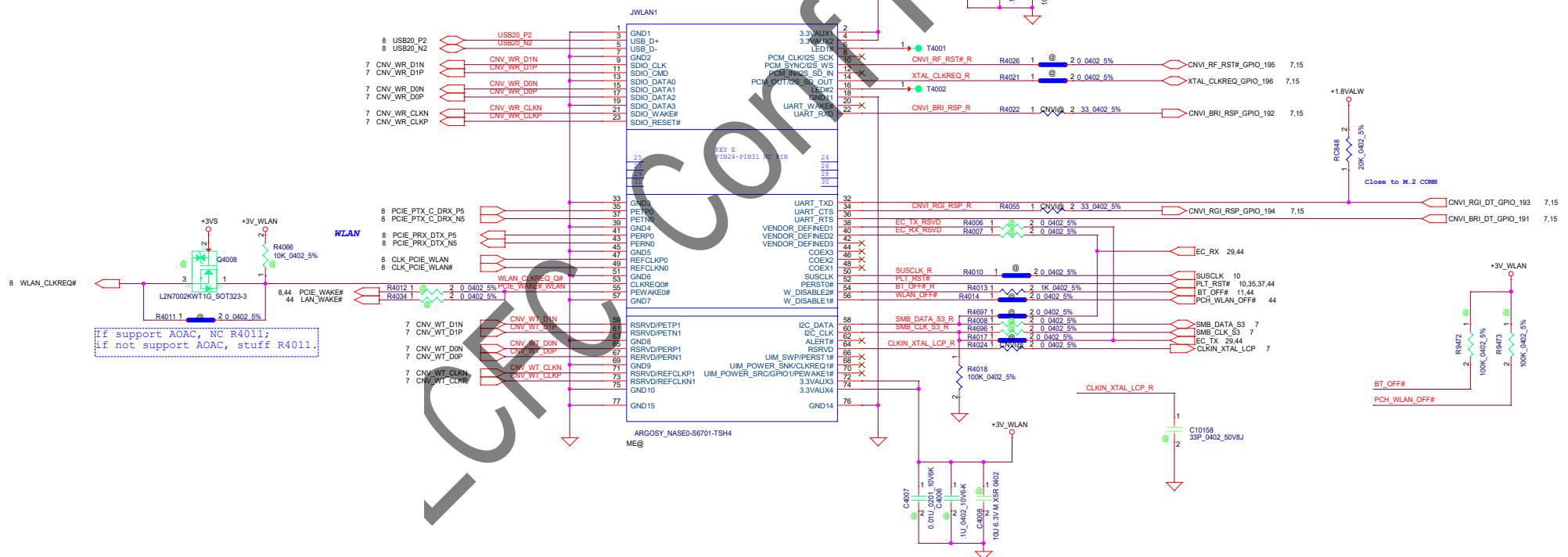
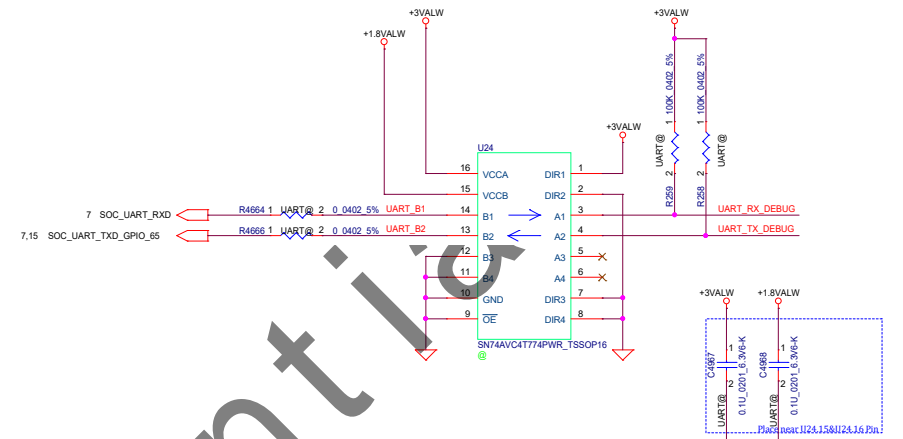
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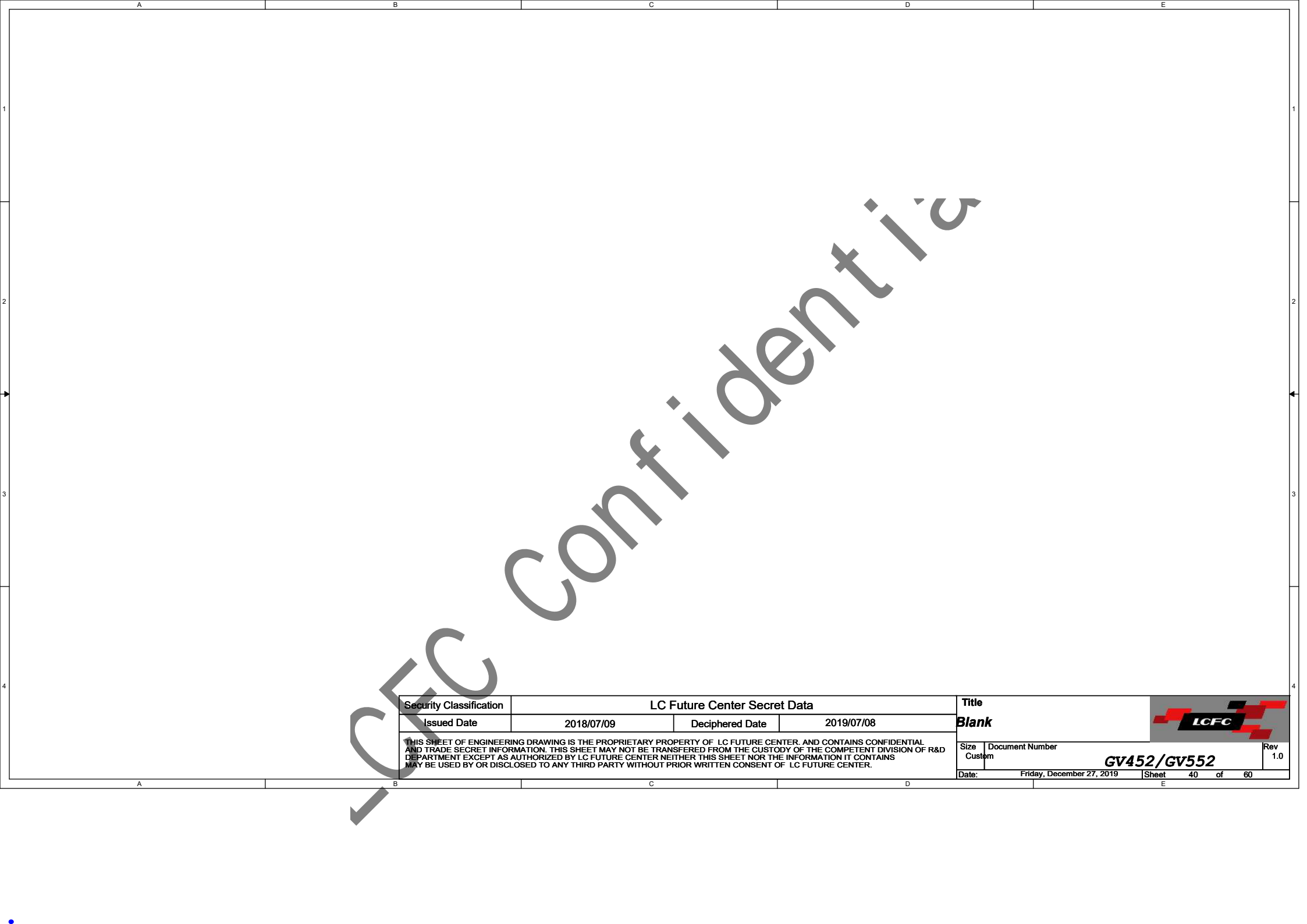
Mini-Express Card(WLAN/WiMAX)




UART Transceiver




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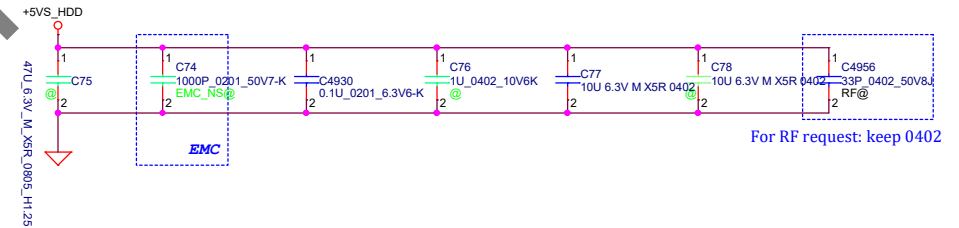
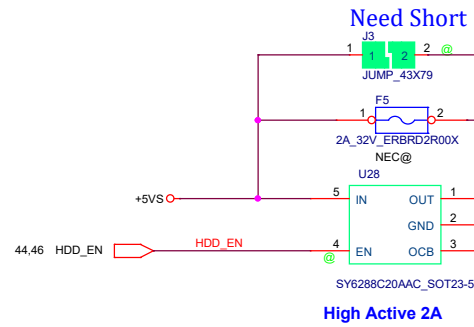
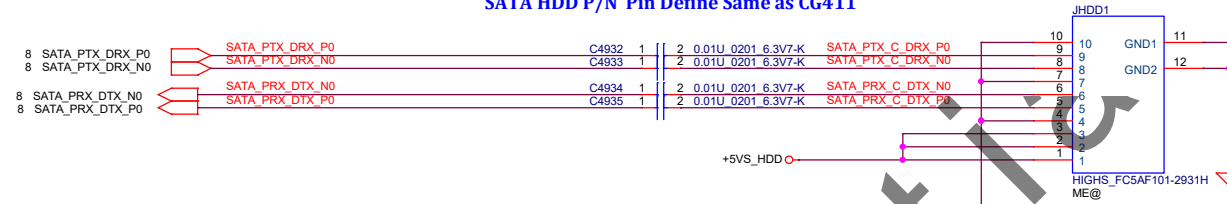
Date:	Friday, December 27, 2019	Sheet	40	of	60
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Custom		GV452/GV552		Sheet 41 of 60		1.0	


SATA HDD Conn.

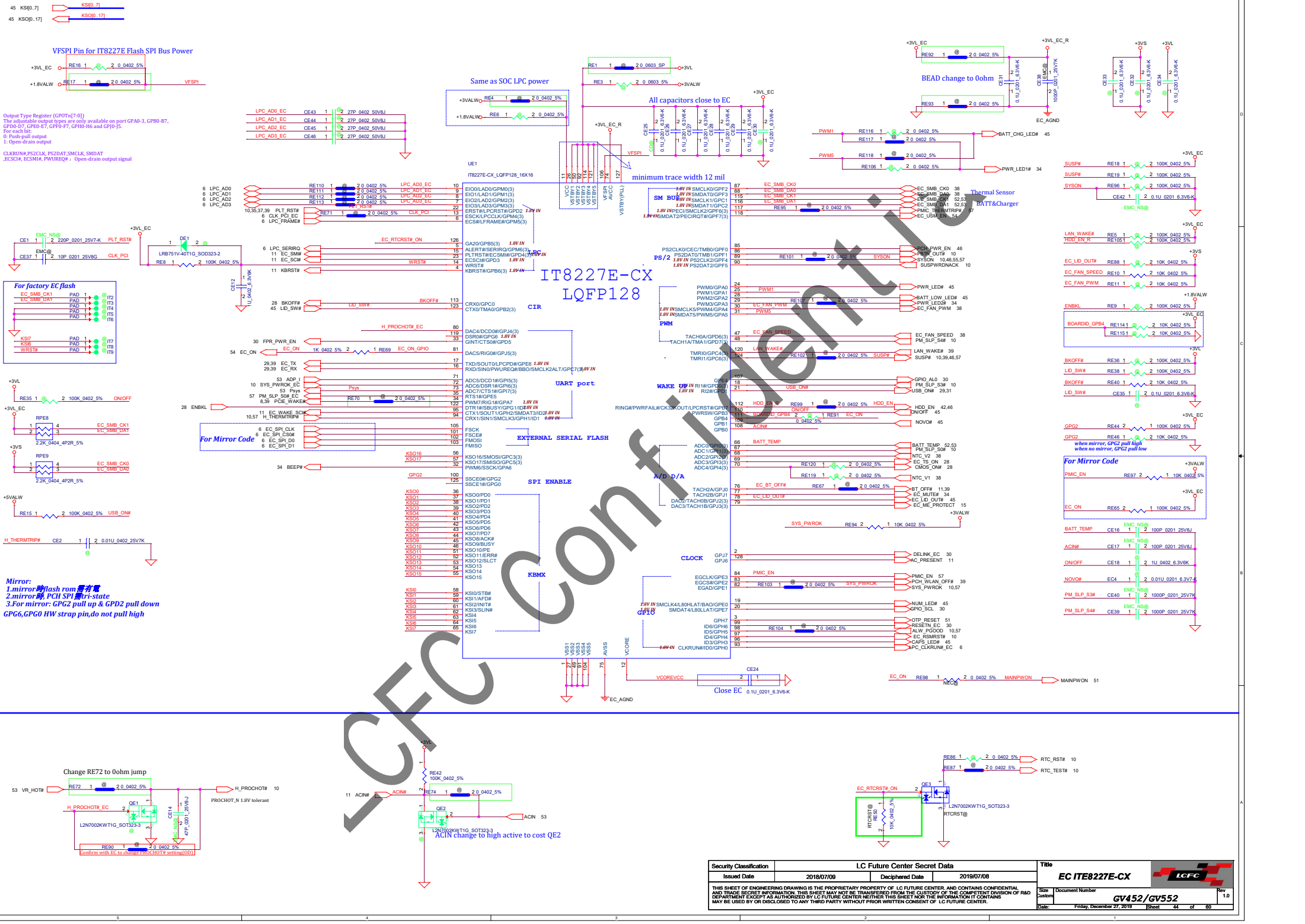
SATA HDD P/N Pin Define Same as CG411



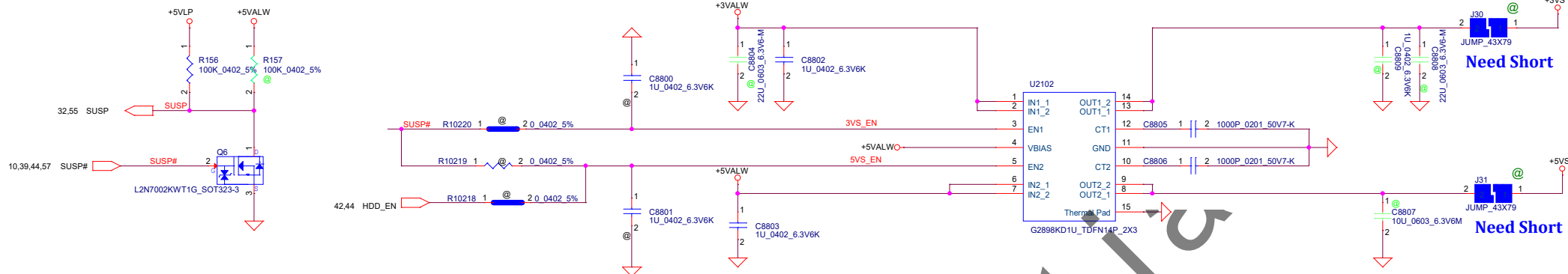
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Issued Date	2018/07/09	Deciphered Date	2019/07/08	Size	Document Number	GV452/GV552	Rev 1.0
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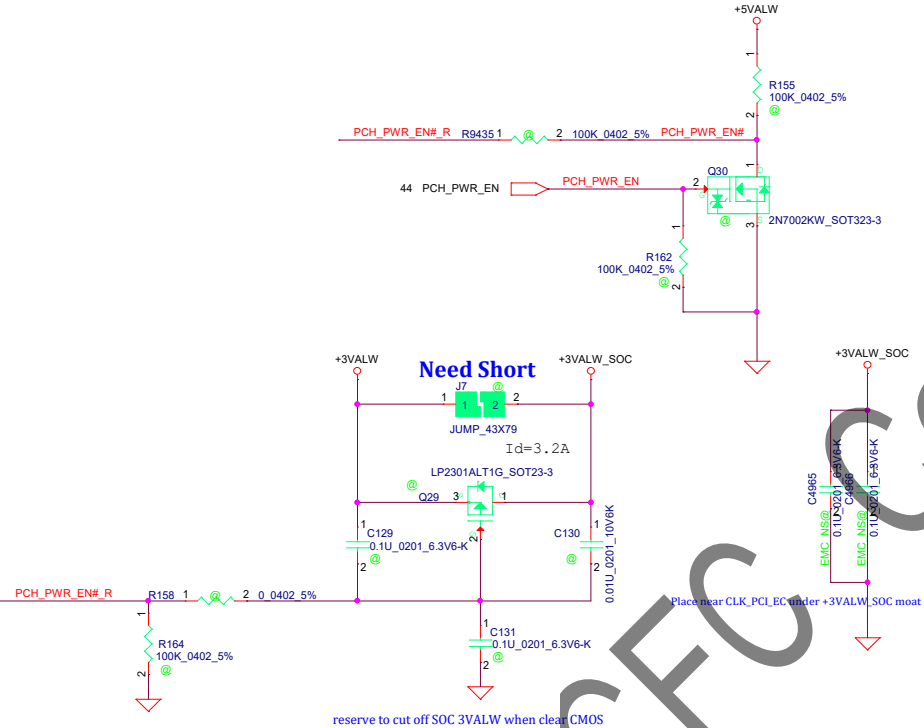
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Issued Date	2018/07/09	Deciphered Date	2019/07/08	Blank			
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				Custom	GV452/GV552	1.0	
				Date:	Friday, December 27, 2018	Sheet	43 of 60



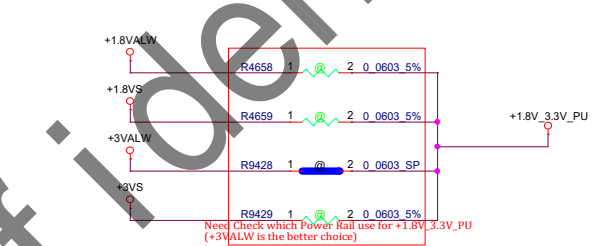
+3VALW to +3VS&+5VALW to +5VS



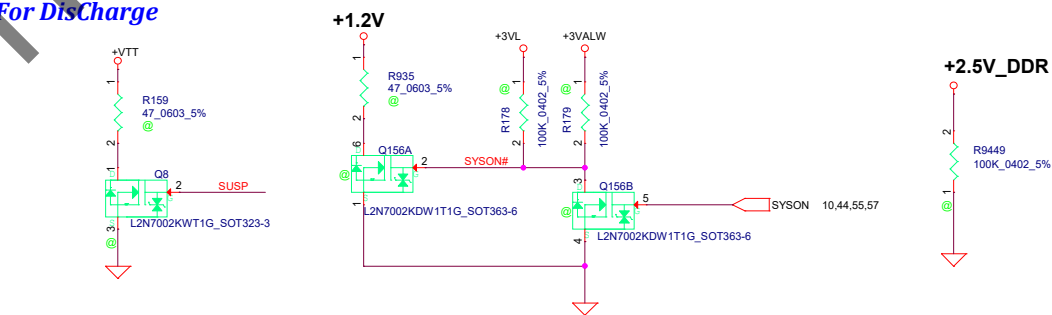
+3VALW to +3VALW_SOC



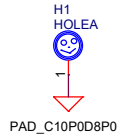
+1.8V_3.3V_PU Power Rail for 1.8/3.3 Select by Soft Strap



For DisCharge



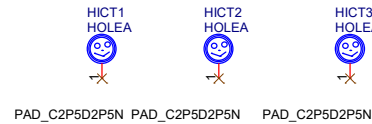
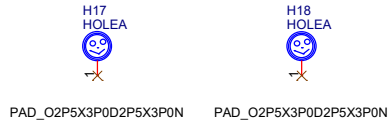
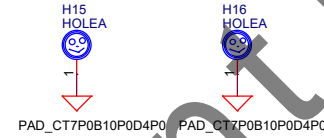
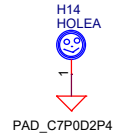
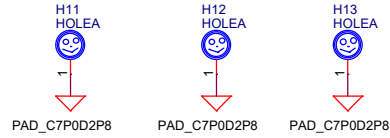
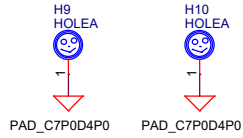
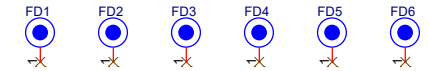
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Issued Date	2018/07/09	Deciphered Date	2019/07/08	DC V TO VS INTERFACE
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Size	Document Number	GV452/GV552		
Customer				
Date	Friday, December 27, 2018	Stage	46	of 60



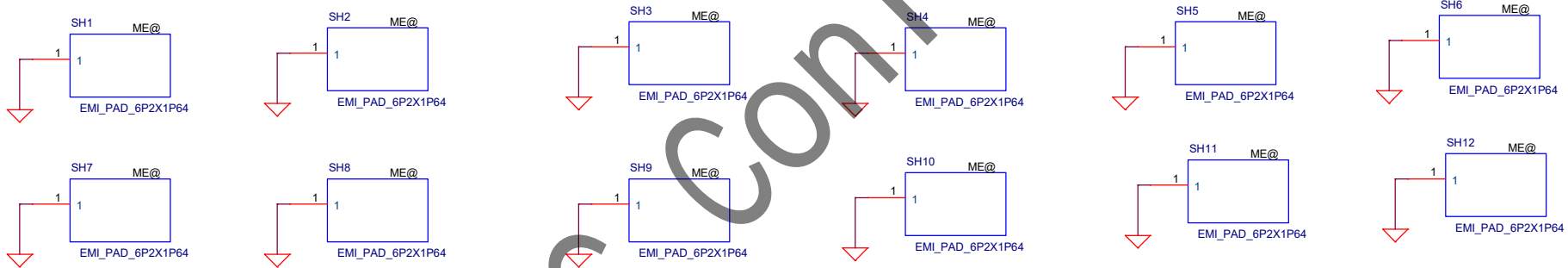
CPU Thermal Holey4




PCB Fedical Mark PAD



Shielding



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				Document Number	GV452/GV552
				Date:	Friday, December 27, 2019
				Sheet	47 of 60
				Rev	1.0

DAZ PCB PN

S350 IGL 14&15 PCB MB

PCB IO/B

HDMI Logo

GLK-R CPU
MP Sample

UC1

N5030

S IC F1808003007442 SRPDC R0 1.1G 121

UC1

N4120

S IC F1808003007400 SRESZ R0 1.1G 121

UC1

N4020

S IC F1808003007426 SPRETO R0 1.1G 121

S350 DRAM X76

ZZ24

DRAM_S4G@

ALT_GROUP PARTS D8G SAM GS453

ZZ25

DRAM_S4G@

ALT_GROUP PARTS D4G SAM GS553

ZZ26

DRAM_M8G@

ALT_GROUP PARTS D8G MIC GS553

ZZ27

DRAM_M8G@

ALT_GROUP PARTS D4G MIC GS553

ZZ28

DRAM_H8G@

ALT_GROUP PARTS D8G HYN GS453

ID4	ID5	ID6	ID7	Description
RC9477	RC9478	RC9479	RC9480	SAMSUNG KA8G16SWC-BCWE
RC9477	RC9478	RC9479	RC9478	MICRON MT40A512M16TB-062EJ
RC9477	RC9478	RC9475	RC9480	HYNIX H5AN8GNCJR-XNC
0	0	1	1	
0	1	0	0	
0	1	0	1	
0	1	1	0	
0	1	1	1	

S350 DRAM X76

ZZ29

VDRAM_M8G@

ALT_GROUP PARTS D8G MIC GV452

ZZ210

VDRAM_H8G@

ALT_GROUP PARTS D4G HYN GV452

ZZ211

VDRAM_S4G@

ALT_GROUP PARTS D4G SAM GV552

UD1

S4G@

S IC D4 512MX16/3200 KA8G16SWC-BCWE 121

UD2

S4G@

S IC D4 512MX16/3200 KA8G16SWC-BCWE 121

UD3

S4G@

S IC D4 512MX16/3200 KA8G16SWC-BCWE 121

UD4

S4G@

S IC D4 512MX16/3200 KA8G16SWC-BCWE 121

UD5

S4G@

S IC D4 512MX16/3200 KA8G16SWC-BCWE 121

UD6

S4G@

S IC D4 512MX16/3200 KA8G16SWC-BCWE 121

UD7

S4G@

S IC D4 512MX16/3200 KA8G16SWC-BCWE 121

UD8

S4G@

S IC D4 512MX16/3200 KA8G16SWC-BCWE 121

RC9477

S4G@

S RES 116W 2.2K +5% 0402

RC9478

S4G@

S RES 116W 2.2K +5% 0402

RC9479

S4G@

S RES 116W 2.2K +5% 0402

RC9480

S4G@

S RES 116W 2.2K +5% 0402

UD1

S4G@

S IC D4 512MX16/3200 KA8G16SWC-BCWE 121

UD2

S4G@

S IC D4 512MX16/3200 KA8G16SWC-BCWE 121

UD3

S4G@

S IC D4 512MX16/3200 KA8G16SWC-BCWE 121

UD4

S4G@

S IC D4 512MX16/3200 KA8G16SWC-BCWE 121

RC9477

S4G@

S RES 116W 2.2K +5% 0402

RC9478

S4G@

S RES 116W 2.2K +5% 0402

RC9479

S4G@

S RES 116W 2.2K +5% 0402

RC9480

S4G@

S RES 116W 2.2K +5% 0402

UD1

M8G@

S IC D4 512M16 MT40A512M16TB-062E-J 121

UD2

M8G@

S IC D4 512M16 MT40A512M16TB-062E-J 121

UD3

M8G@

S IC D4 512M16 MT40A512M16TB-062E-J 121

UD4

M8G@

S IC D4 512M16 MT40A512M16TB-062E-J 121

UD5

M8G@

S IC D4 512M16 MT40A512M16TB-062E-J 121

UD6

M8G@

S IC D4 512M16 MT40A512M16TB-062E-J 121

UD7

M8G@

S IC D4 512M16 MT40A512M16TB-062E-J 121

UD8

M8G@

S IC D4 512M16 MT40A512M16TB-062E-J 121

RC9477

M8G@

S RES 116W 2.2K +5% 0402

RC9478

M8G@

S RES 116W 2.2K +5% 0402

RC9479

M8G@

S RES 116W 2.2K +5% 0402

RC9480

M8G@

S RES 116W 2.2K +5% 0402

UD1

M8G@

S IC D4 512M16 MT40A512M16TB-062E-J 121

UD2

M8G@

S IC D4 512M16 MT40A512M16TB-062E-J 121

UD3

M8G@

S IC D4 512M16 MT40A512M16TB-062E-J 121

UD4

M8G@

S IC D4 512M16 MT40A512M16TB-062E-J 121

RC9477

M8G@

S RES 116W 2.2K +5% 0402

RC9478

M8G@

S RES 116W 2.2K +5% 0402

RC9479

M8G@

S RES 116W 2.2K +5% 0402

RC9480

M8G@

S RES 116W 2.2K +5% 0402

UD1

H8G@

S IC D4 512MX16/3200 H5AN8GNCJR-XNC 121

UD2

H8G@

S IC D4 512MX16/3200 H5AN8GNCJR-XNC 121

UD3

H8G@

S IC D4 512MX16/3200 H5AN8GNCJR-XNC 121

UD4

H8G@

S IC D4 512MX16/3200 H5AN8GNCJR-XNC 121

UD5

H8G@

S IC D4 512MX16/3200 H5AN8GNCJR-XNC 121

UD6

H8G@

S IC D4 512MX16/3200 H5AN8GNCJR-XNC 121

UD7

H8G@

S IC D4 512MX16/3200 H5AN8GNCJR-XNC 121

UD8

H8G@

S IC D4 512MX16/3200 H5AN8GNCJR-XNC 121

RC9477

H8G@

S RES 116W 2.2K +5% 0402

RC9478

H8G@

S RES 116W 2.2K +5% 0402

RC9479

H8G@

S RES 116W 2.2K +5% 0402

RC9480

H8G@

S RES 116W 2.2K +5% 0402

UD1

H4G@

S IC D4 512MX16/3200 H5AN8GNCJR-XNC 121

UD2

H4G@

S IC D4 512MX16/3200 H5AN8GNCJR-XNC 121

UD3

H4G@

S IC D4 512MX16/3200 H5AN8GNCJR-XNC 121

UD4

H4G@

S IC D4 512MX16/3200 H5AN8GNCJR-XNC 121

RC9477

H4G@

S RES 116W 2.2K +5% 0402

RC9478

H4G@

S RES 116W 2.2K +5% 0402

RC9479

H4G@


S RES 116W 2.2K +5% 0402

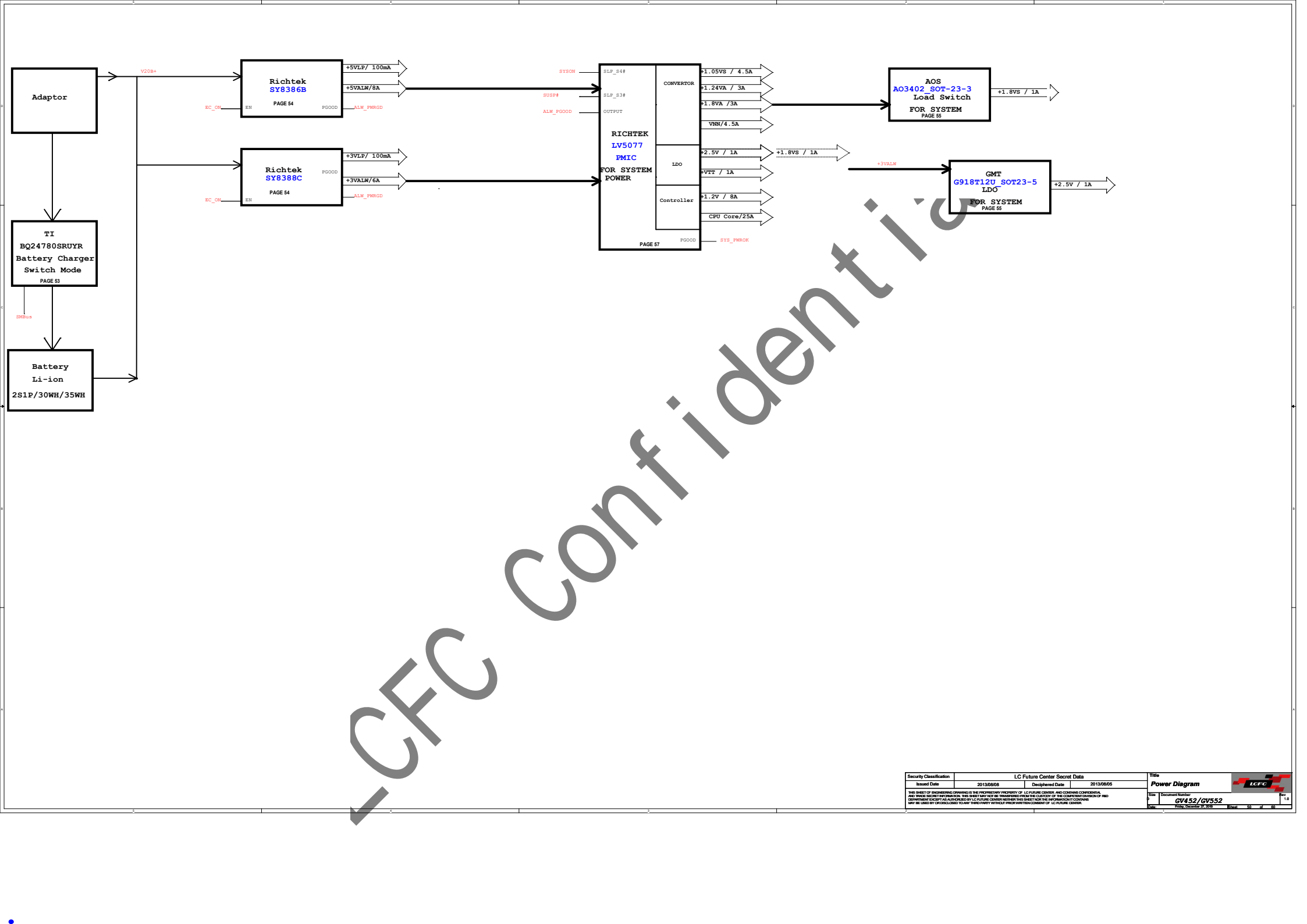
RC9480

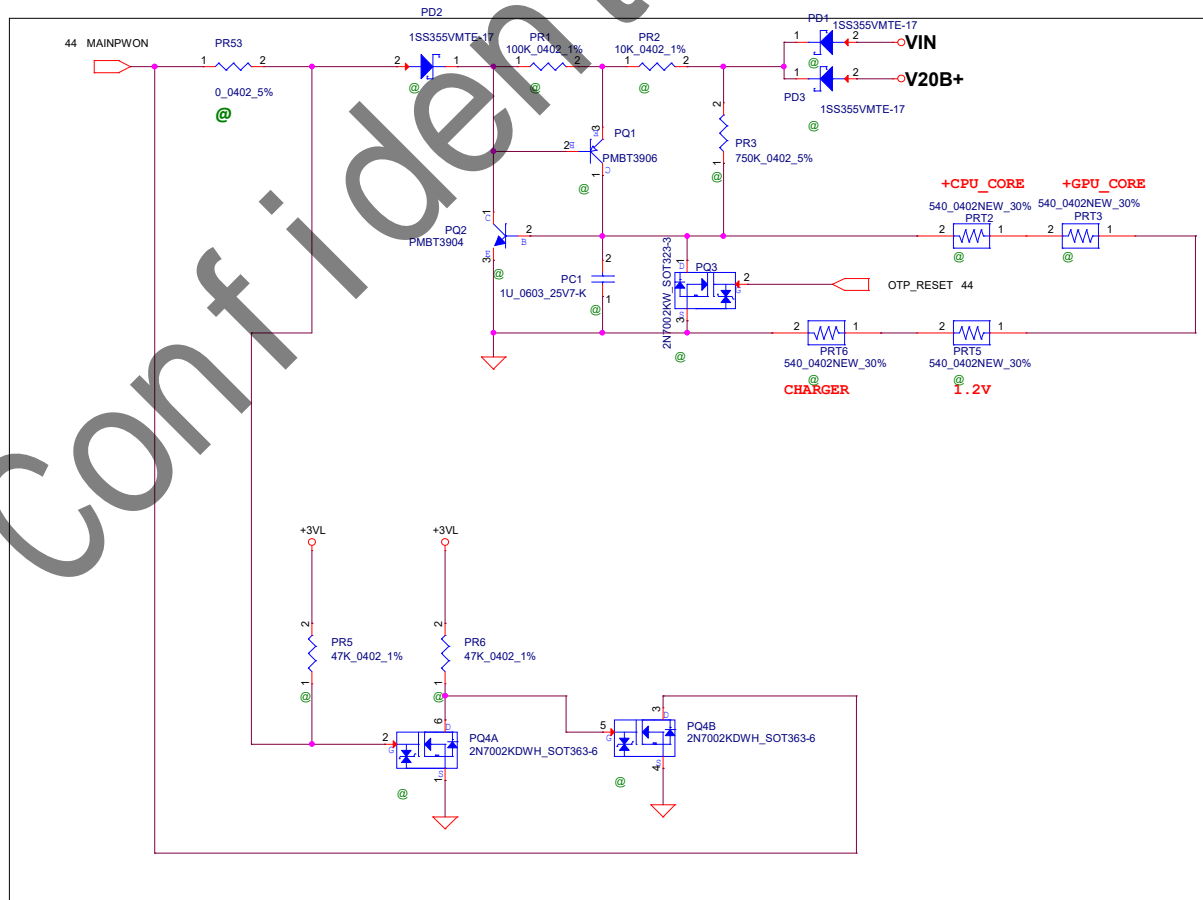
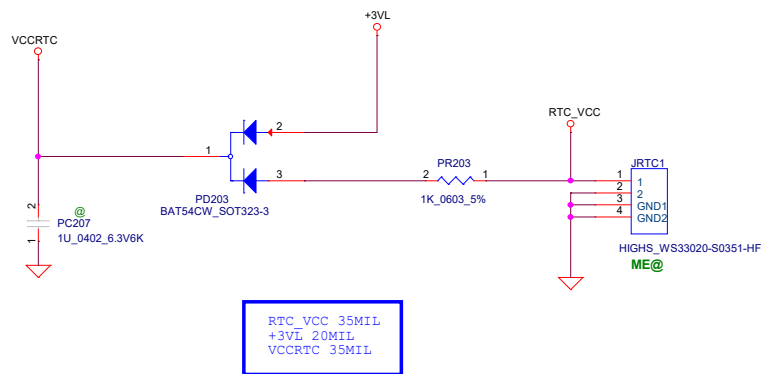
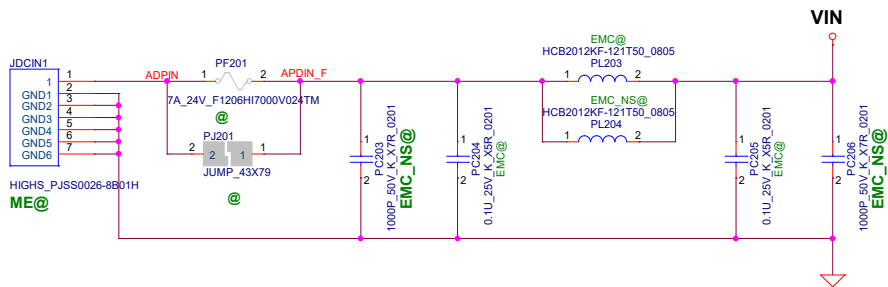
H4G@

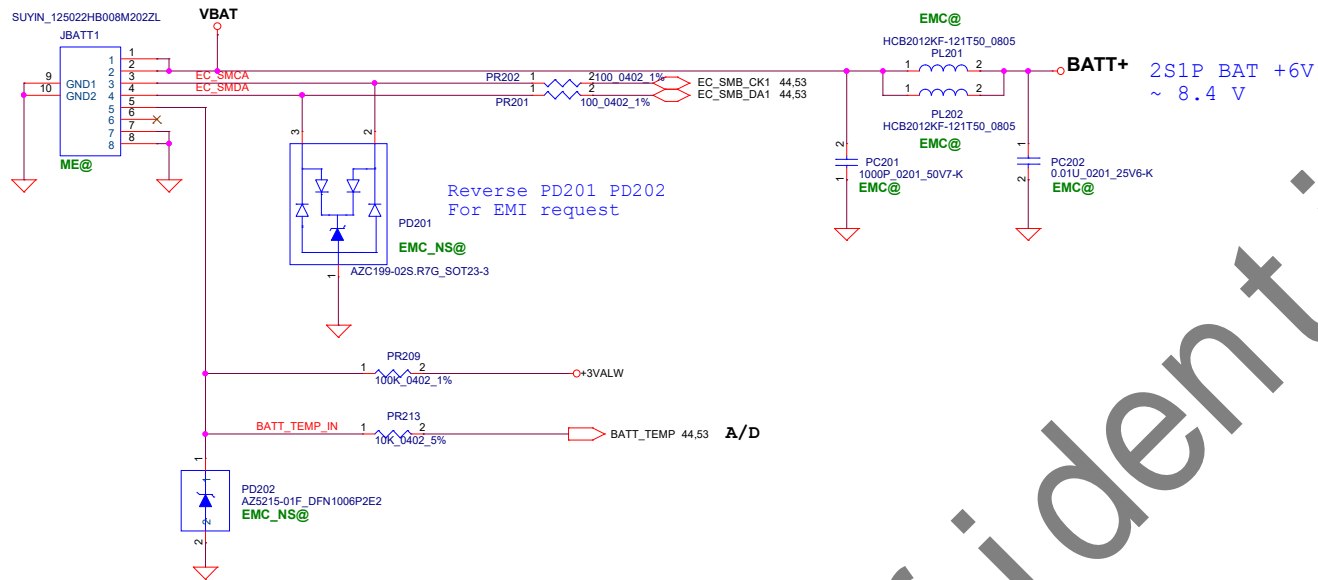
S RES 116W 2.2K +5% 0402

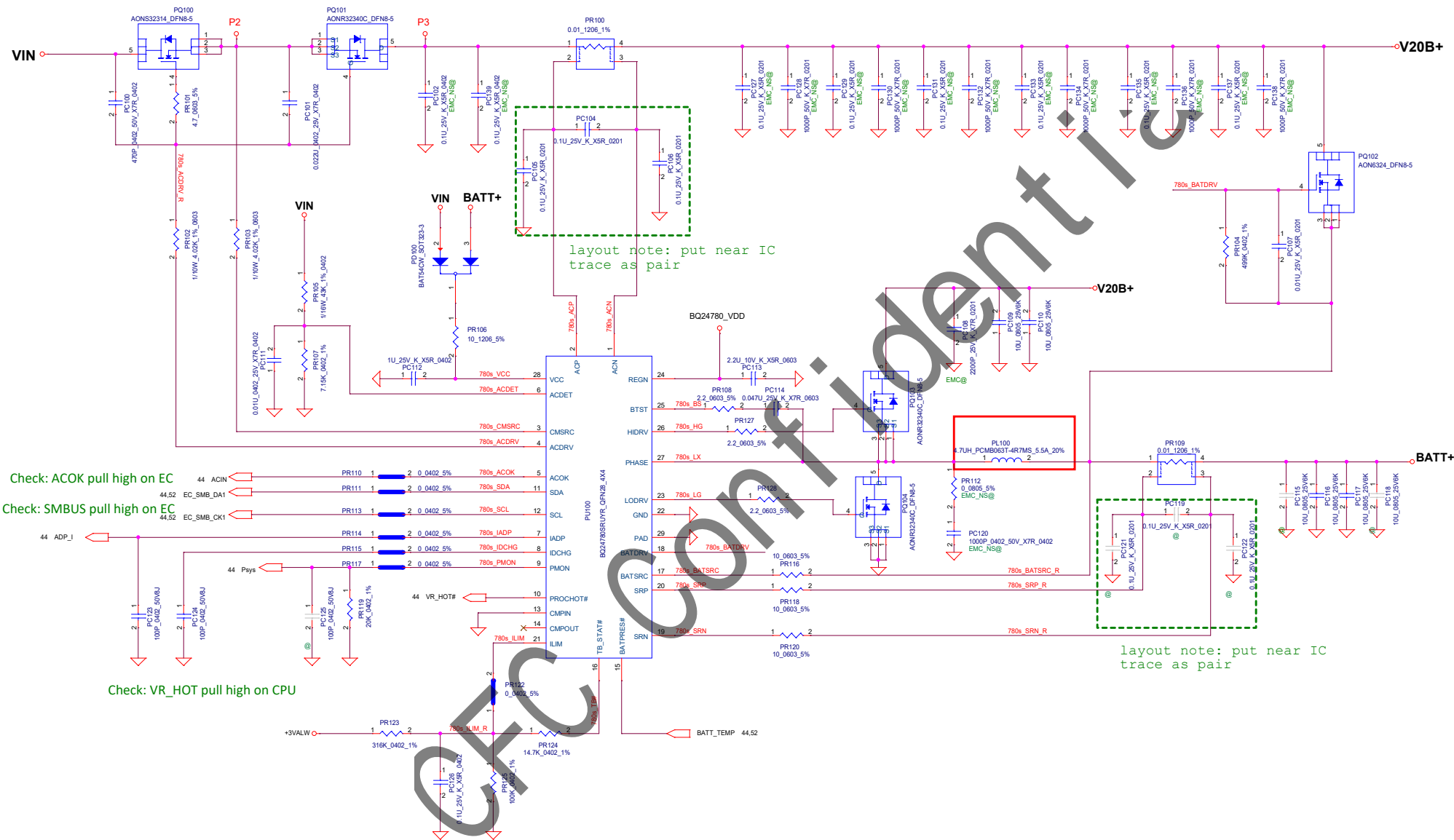
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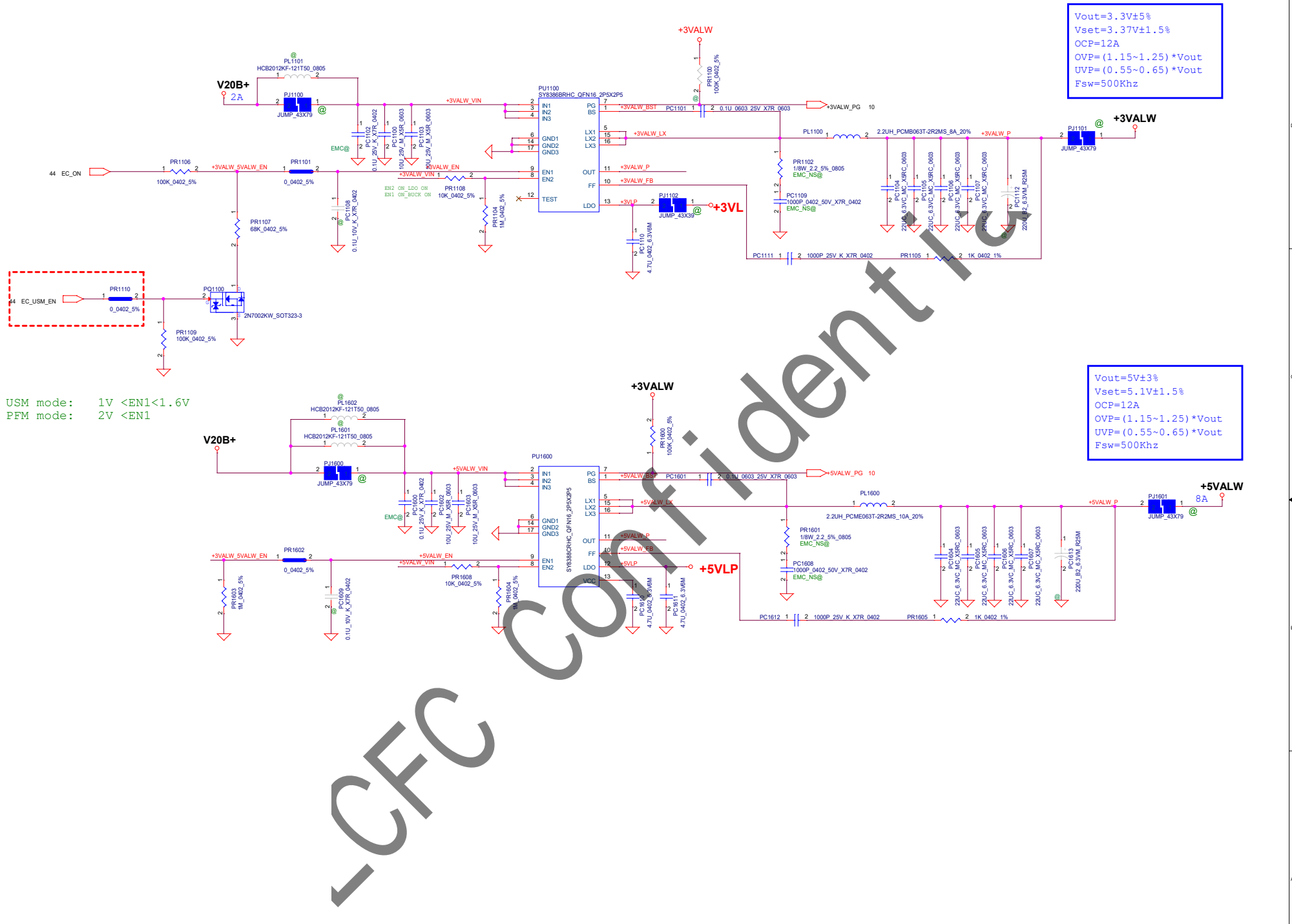
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				Date:	Friday, December 27, 2018	Sheet	49 of 60












USM mode: 1V <EN1<1.6V
PFM mode: 2V <EN1


Vout=3.3V±5%
Vset=3.37V±1.5%
OCP=12A
OVP=(1.15~1.25)*Vout
UVP=(0.55~0.65)*Vout
Fsw=500Khz

Vout=5V±3%
Vset=5.1V±1.5%
OCP=12A
OVP=(1.15~1.25)*Vout
UVP=(0.55~0.65)*Vout
Fsw=500Khz


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				Date	Friday, December 27, 2018	Sheet	59	of	60

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				Date	Friday, December 27, 2018	Sheet	60	of	60